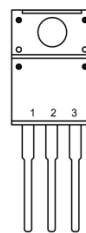
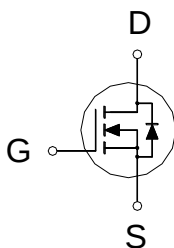


PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
150V	9.3m Ω	50A



Top View



Side View



1. GATE
2. DRAIN
3. SOURCE

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	150	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C = 25\text{ }^{\circ}\text{C}$	I_D	50	A
	$T_C = 100\text{ }^{\circ}\text{C}$		36	
Pulsed Drain Current ¹		I_{DM}	260	
Avalanche Current		I_{AS}	20	
Avalanche Energy	$L = 1\text{mH}$	E_{AS}	200	mJ
Power Dissipation	$T_C = 25\text{ }^{\circ}\text{C}$	P_D	60	W
	$T_C = 100\text{ }^{\circ}\text{C}$		30	
Operating Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 175	$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	Steady-State	$R_{\theta JA}$		62.5	
Junction-to-Case	Steady-State	$R_{\theta JC}$		2.5	

¹Pulse width limited by maximum junction temperature.

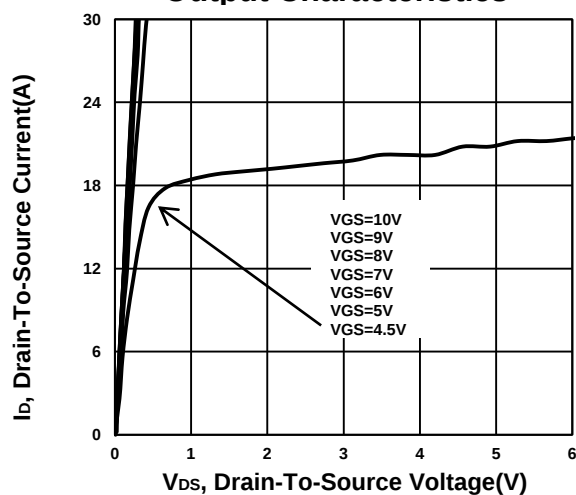
ELECTRICAL CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	150			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2	3	4	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA

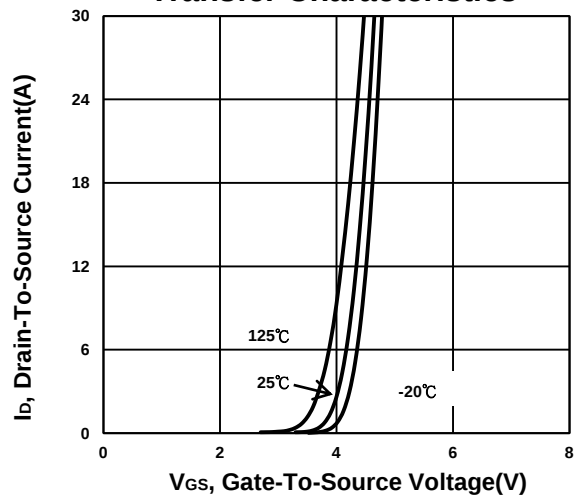
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 150V, V _{GS} = 0V			1	μA
		V _{DS} = 150V, V _{GS} = 0V, T _J = 125 ° C			100	
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 10V, I _D = 20A		7.9	9.3	mΩ
Forward Transconductance ¹	g _{fs}	V _{DS} = 5V, I _D = 20A		53		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 75V, f = 1MHz		3597		pF
Output Capacitance	C _{oss}			310		
Reverse Transfer Capacitance	C _{rss}			7		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		1.2		Ω
Total Gate Charge ²	Q _g	V _{GS} = 10V, V _{DS} = 75V,I _D = 20A		53		nC
Gate-Source Charge ²	Q _{gs}			16		
Gate-Drain Charge ²	Q _{gd}			13		
Turn-On Delay Time ²	t _{d(on)}	V _{DD} = 75V, I _D ≅ 20A, V _{GS} = 10V, R _{GEN} =6Ω		21		nS
Rise Time ²	t _r			51		
Turn-Off Delay Time ²	t _{d(off)}			46		
Fall Time ²	t _f			59		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T _J = 25 °C)						
Continuous Current	I _S				50	A
Forward Voltage ¹	V _{SD}	I _F = 20A , V _{GS} = 0V			1.2	V
Reverse Recovery Time	t _{rr}	I _F = 20A , dI _F /dt= 100A/μs		100		nS
Reverse Recovery Charge	Q _{rr}			227		nC

¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.²Independent of operating temperature.

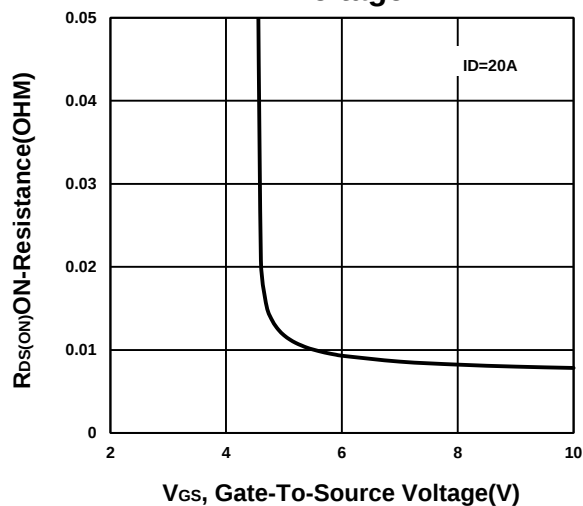
Output Characteristics



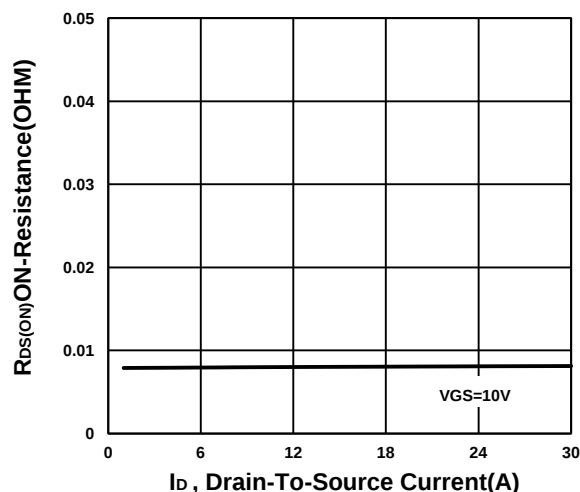
Transfer Characteristics



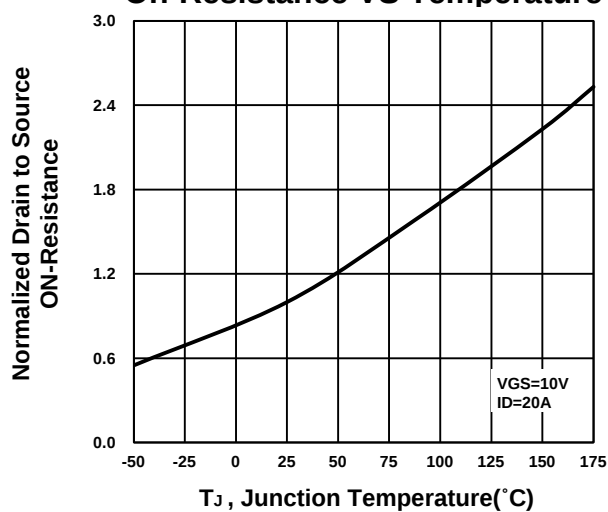
On-Resistance VS Gate-To-Source Voltage



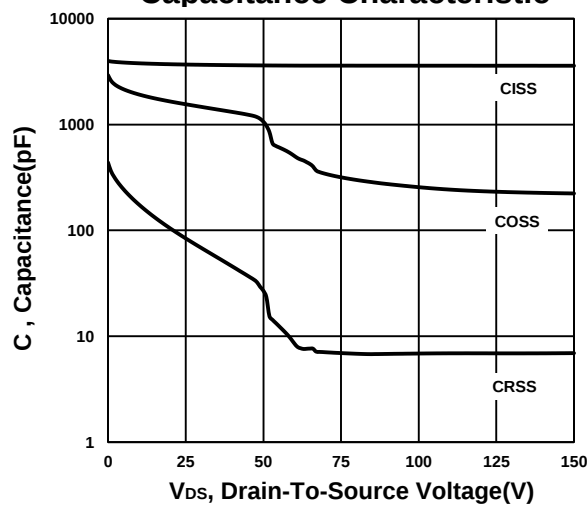
On-Resistance VS Drain Current



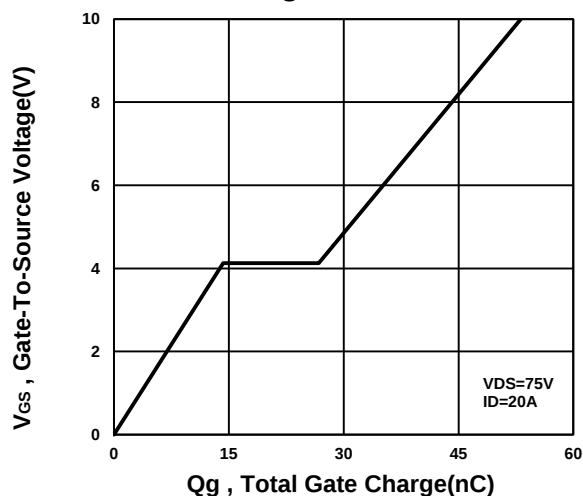
On-Resistance VS Temperature



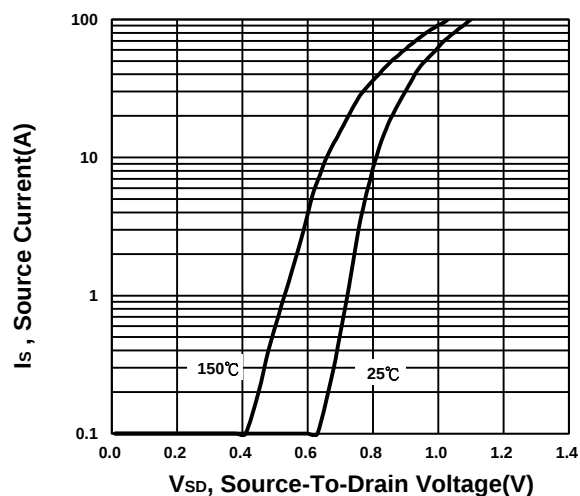
Capacitance Characteristic



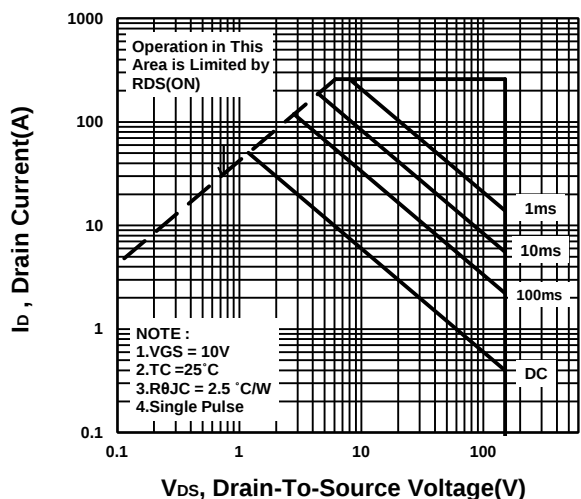
Gate charge Characteristics



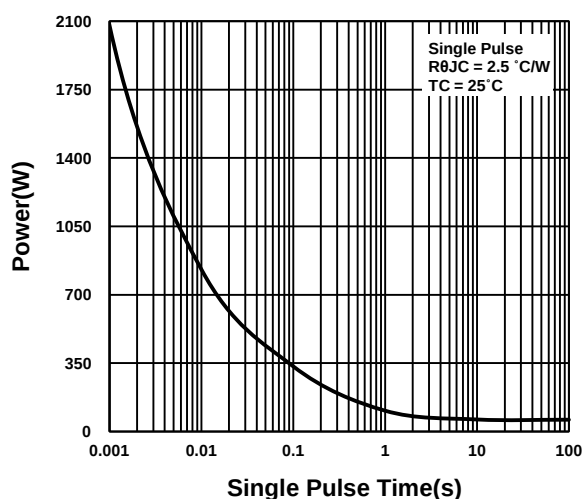
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

