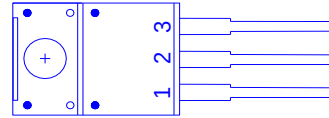
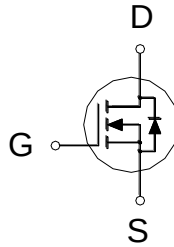




PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	140m Ω	10A



1. GATE
2. DRAIN
3. SOURCE

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C = 25\text{ }^{\circ}\text{C}$	I_D	10	A
	$T_C = 100\text{ }^{\circ}\text{C}$		6	
Pulsed Drain Current ¹		I_{DM}	30	
Avalanche Current		I_{AS}	9.8	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	4.8	mJ
Power Dissipation	$T_C = 25\text{ }^{\circ}\text{C}$	P_D	29	W
	$T_C = 100\text{ }^{\circ}\text{C}$		11	
Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		4.2	$^{\circ}\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

ELECTRICAL CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$, Unless Otherwise Noted)

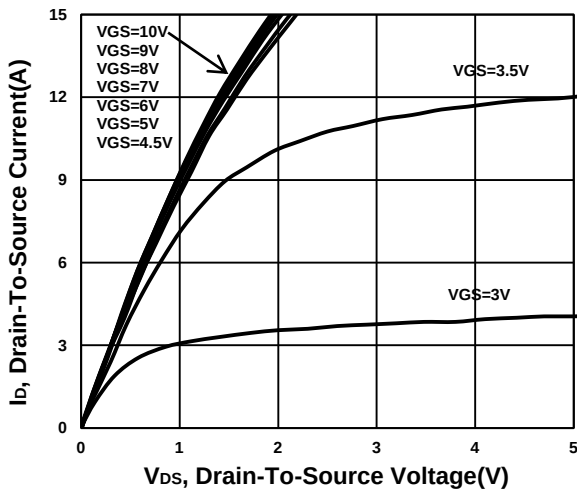
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.3	1.8	2.3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 80V, V_{GS} = 0V$			1	μA
		$V_{DS} = 80V, V_{GS} = 0V, T_J = 125\text{ }^{\circ}C$			10	
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 5A$		105	170	mΩ
		$V_{GS} = 10V, I_D = 5A$		95	140	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 10V, I_D = 5A$		13		S

DYNAMIC						
Input Capacitance	C _{iSS}	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz		333		pF
Output Capacitance	C _{oSS}			59		
Reverse Transfer Capacitance	C _{rSS}			26		
Total Gate Charge ²	Q _g	V _{GS} = 10V , V _{DS} = 50V, I _D = 5A		9		nC
Gate-Source Charge ²	Q _{gs}			1.1		
Gate-Drain Charge ²	Q _{gd}			4		
Turn-On Delay Time ²	t _{d(on)}	V _{DS} = 50V I _D ≅ 5A, V _{GS} = 10V, R _{GEN} =6 Ω		22		nS
Rise Time ²	t _r			60		
Turn-Off Delay Time ²	t _{d(off)}			30		
Fall Time ²	t _f			40		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T _J = 25 °C)						
Continuous Current	I _S				10	A
Forward Voltage ¹	V _{SD}	I _F = 5A, V _{GS} = 0V			1.1	V
Reverse Recovery Time	t _{rr}	I _F = 5A, dI _F /dt = 100A / μS		29		nS
Reverse Recovery Charge	Q _{rr}			33		nC

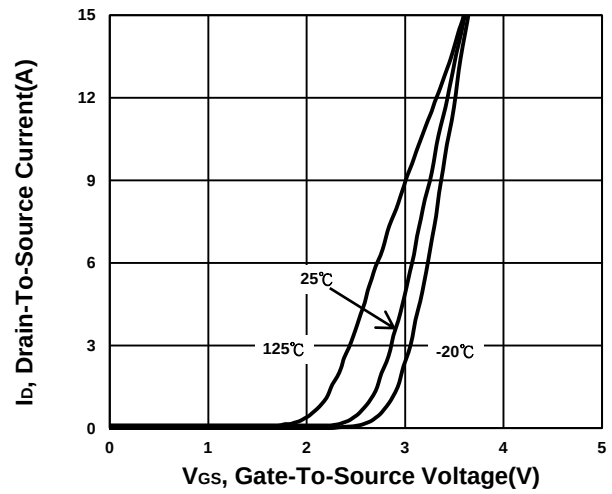
¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

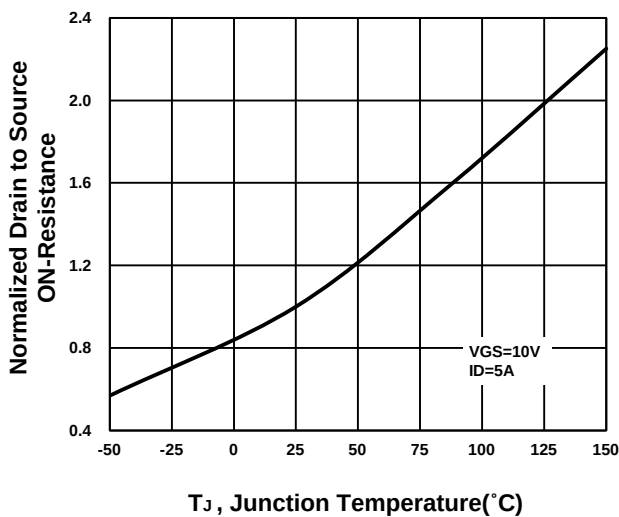
Output Characteristics



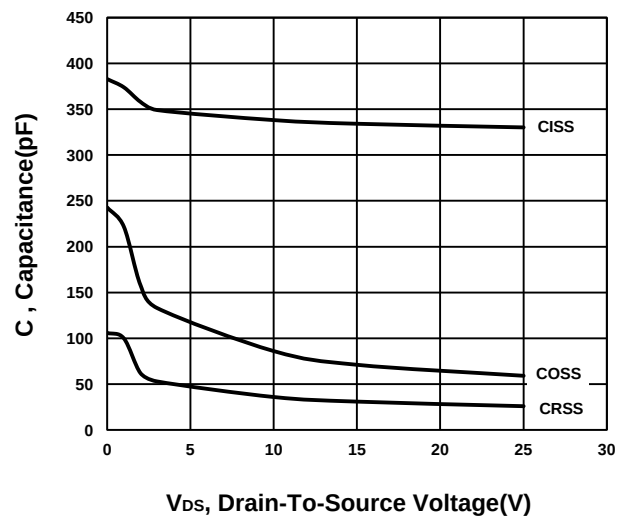
Transfer Characteristics



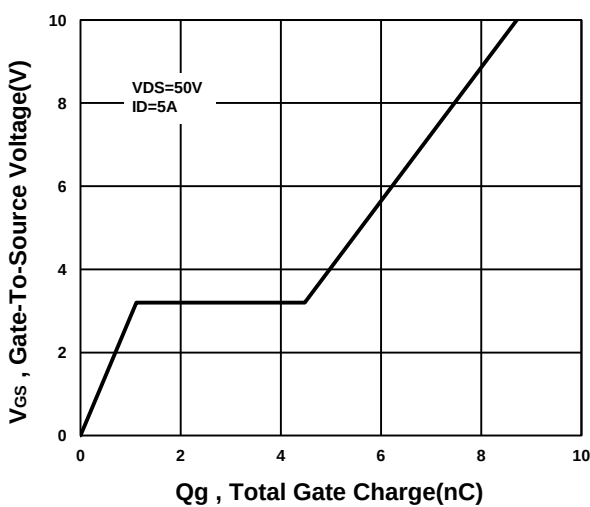
On-Resistance VS Temperature



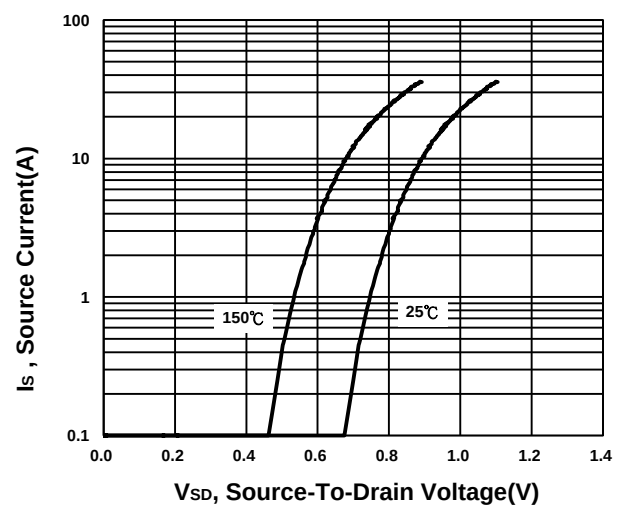
Capacitance Characteristic



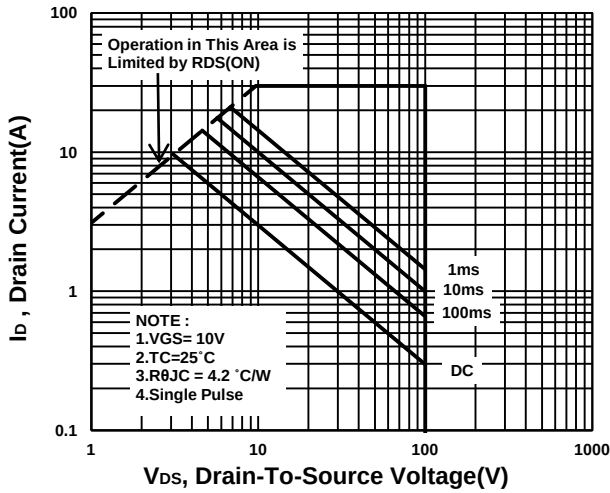
Gate charge Characteristics



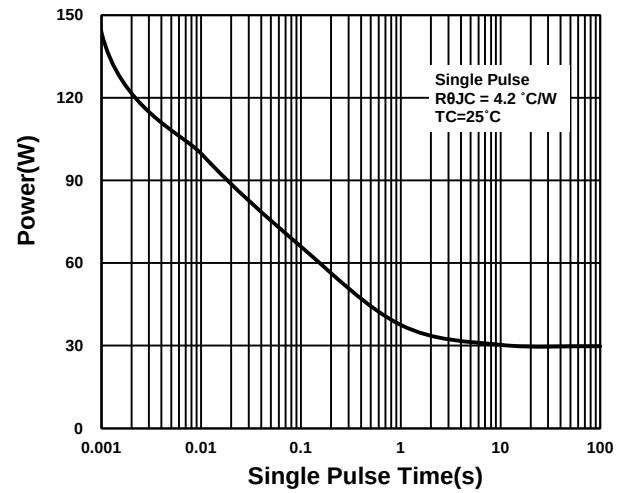
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

