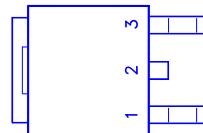
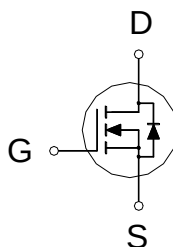


PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
700V	1.63Ω	4A



1. GATE
2. DRAIN
3. SOURCE

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	700	V
Gate-Source Voltage		V_{GS}	± 30	V
Continuous Drain Current ²	$T_C = 25^\circ\text{C}$	I_D	4	A
	$T_C = 100^\circ\text{C}$		2.3	
Pulsed Drain Current ¹		I_{DM}	16	
Avalanche Current ³		I_{AS}	1	
Avalanche Energy ³		E_{AS}	22	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	54	W
	$T_C = 100^\circ\text{C}$		22	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.3	$^\circ\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

²Ensure that the channel temperature does not exceed 150°C .

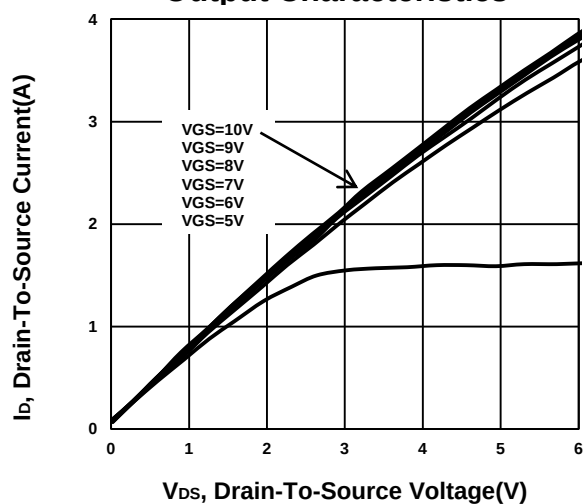
³ $V_{DD} = 50\text{V}$, $L = 40\text{mH}$, starting $T_j = 25^\circ\text{C}$.

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

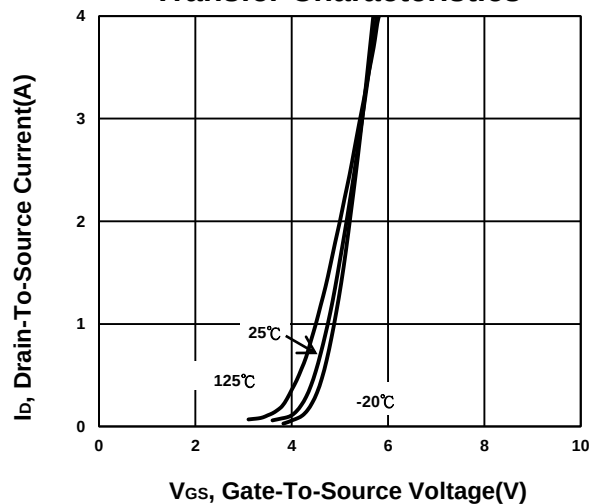
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	700			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2	3.3	4	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 30V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 700V, V_{GS} = 0V$			1	μA
		$V_{DS} = 560V, V_{GS} = 0V, T_J = 100^\circ C$			10	
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 2A$		1.4	1.63	Ω
Forward Transconductance ¹	g_{fs}	$V_{DS} = 15V, I_D = 2A$		2.9		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 100V, f = 250KHz$		228		pF
Output Capacitance	C_{oss}			22		
Reverse Transfer Capacitance	C_{rss}			4.5		
Gate Resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		21		Ω
Total Gate Charge ²	Q_g	$V_{DS} = 560V, V_{GS} = 10V, I_D = 2A$		9.8		nC
Gate-Source Charge ²	Q_{gs}			1.4		
Gate-Drain Charge ²	Q_{gd}			5.1		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DD} = 350V,$ $I_D \cong 2A, V_{GS} = 10V, R_{GEN} = 25\Omega$		15		nS
Rise Time ²	t_r			29		
Turn-Off Delay Time ²	$t_{d(off)}$			44		
Fall Time ²	t_f			69		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ C$)						
Continuous Current	I_S				4	A
Forward Voltage ¹	V_{SD}	$I_F = 4A, V_{GS} = 0V$			1.2	V
Reverse Recovery Time	t_{rr}	$I_F = 2A, di_F/dt = 100A/\mu s$		191		nS
Reverse Recovery Charge	Q_{rr}			1.1		μC

¹Pulse test : Pulse Width $\leq 300 \mu\text{sec}$, Duty Cycle $\leq 2\%$.²Independent of operating temperature.

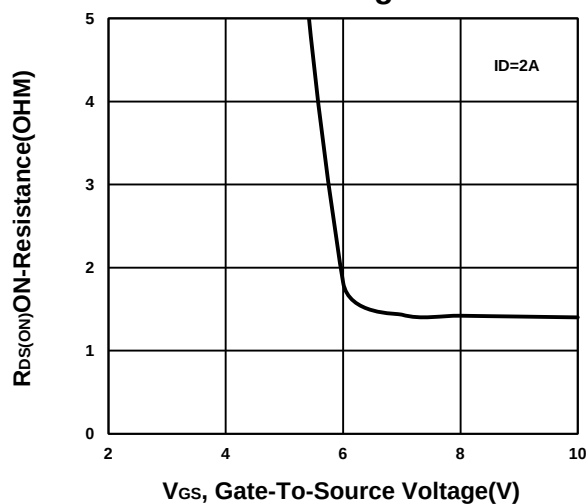
Output Characteristics



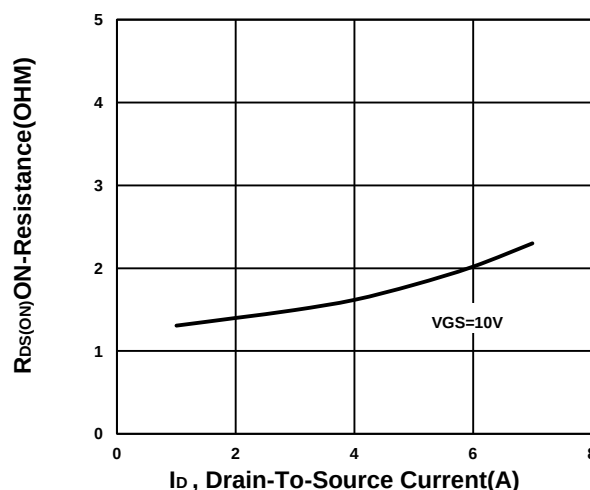
Transfer Characteristics



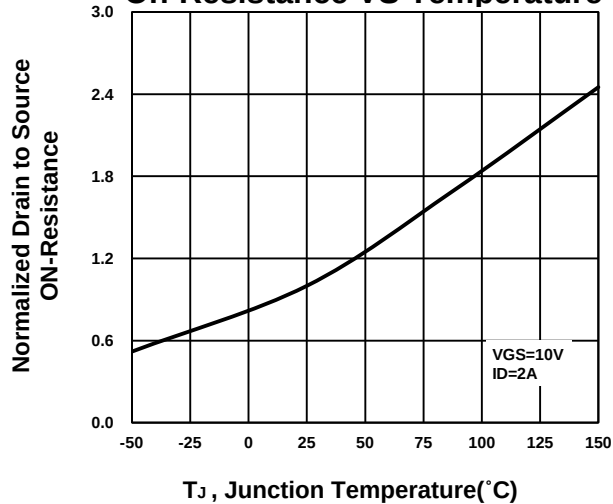
On-Resistance VS Gate-To-Source Voltage



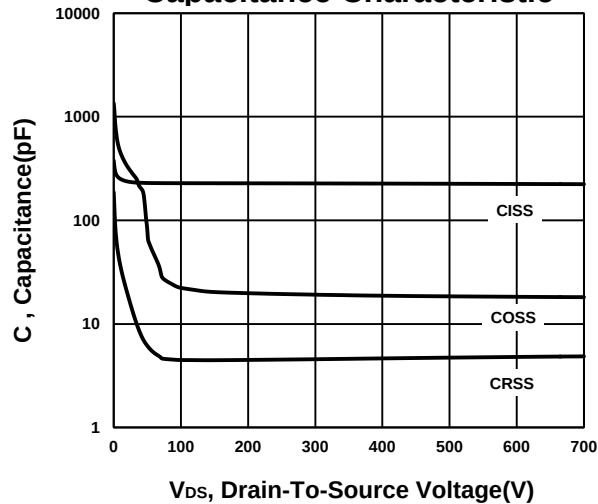
On-Resistance VS Drain Current



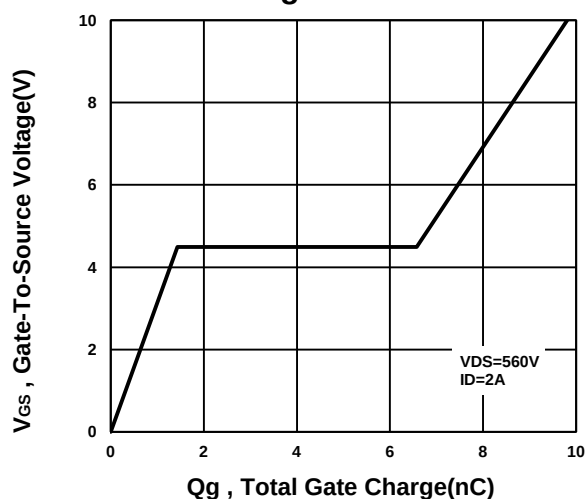
On-Resistance VS Temperature



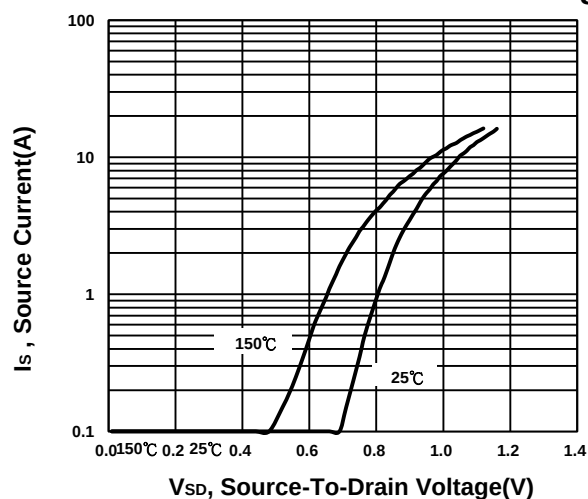
Capacitance Characteristic



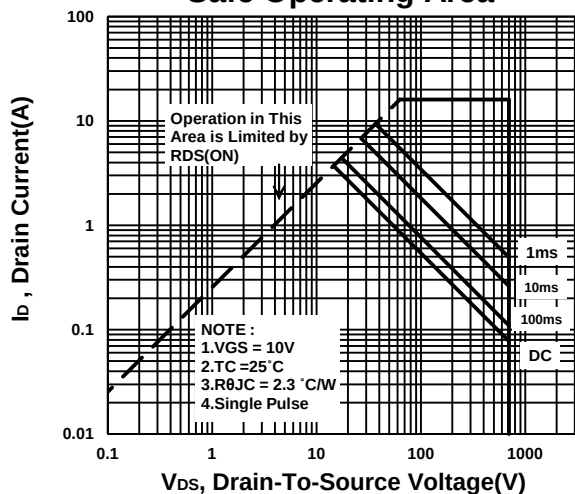
Gate charge Characteristics



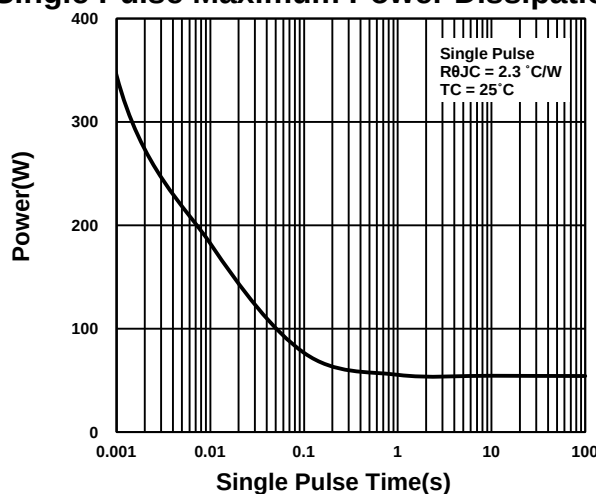
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

