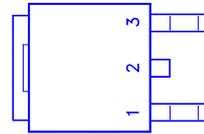
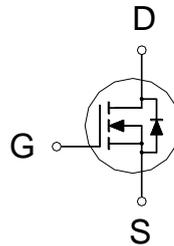


PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
650V	600m Ω	7A



- 1. GATE
- 2. DRAIN
- 3. SOURCE



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	650	V
Gate-Source Voltage		V_{GS}	± 30	V
Continuous Drain Current ²	$T_C = 25^\circ\text{C}$	I_D	7	A
	$T_C = 100^\circ\text{C}$		4	
Pulsed Drain Current ¹		I_{DM}	20	
Avalanche Current ³		I_{AS}	1.7	
Avalanche Energy ³		E_{AS}	108	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	63	W
	$T_C = 100^\circ\text{C}$		25	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2	$^\circ\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

²Ensure that the channel temperature does not exceed 150 $^\circ\text{C}$.

³ $V_{DD} = 50\text{V}$, $L = 75\text{mH}$, starting $T_j = 25^\circ\text{C}$.

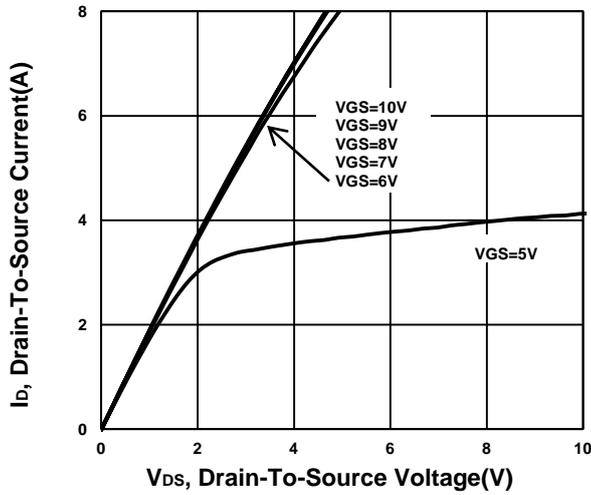
ELECTRICAL CHARACTERISTICS (T_J = 25 ° C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	650			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2	3.2	4	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±30V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 650V, V _{GS} = 0V			1	μA
		V _{DS} = 520V, V _{GS} = 0V, T _J = 100 ° C			10	
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 10V, I _D = 3.5 A		520	600	mΩ
Forward Transconductance ¹	g _{fs}	V _{DS} = 15V, I _D = 3.5A		7		S
DYNAMIC						
Input Capacitance	C _{iSS}	V _{GS} = 0V, V _{DS} = 100V, f = 250KHz		537		pF
Output Capacitance	C _{oSS}			35		
Reverse Transfer Capacitance	C _{rSS}			5		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		20		Ω
Total Gate Charge ²	Q _g	V _{DS} = 520V, V _{GS} = 10V, I _D = 3.5A		18		nC
Gate-Source Charge ²	Q _{gs}			3.3		
Gate-Drain Charge ²	Q _{gd}			8.4		
Turn-On Delay Time ²	t _{d(on)}	V _{DD} = 325V, I _D ≅ 3.5A, V _{GS} = 10V, R _{GEN} = 25Ω		12		nS
Rise Time ²	t _r			26		
Turn-Off Delay Time ²	t _{d(off)}			67		
Fall Time ²	t _f			52		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 ° C)						
Continuous Current	I _S				7	A
Forward Voltage ¹	V _{SD}	I _F = 7A, V _{GS} = 0V			1.2	V
Reverse Recovery Time	t _{rr}	I _F = 3.5A, di _F /dt = 100A/μs		216		nS
Reverse Recovery Charge	Q _{rr}				1.8	

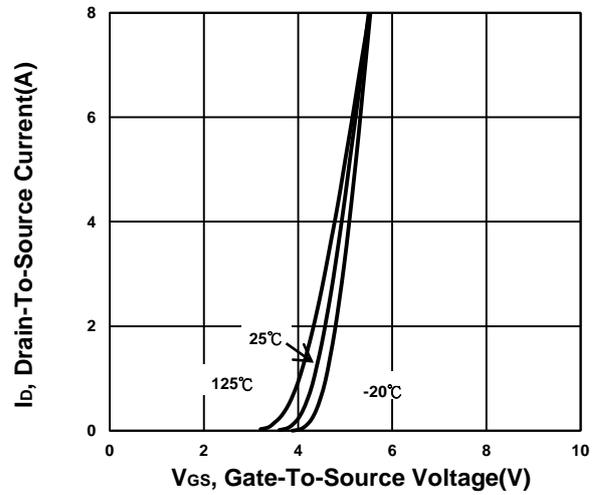
¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

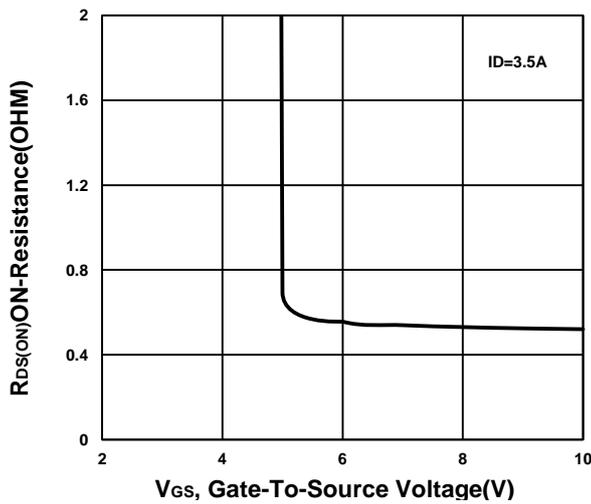
Output Characteristics



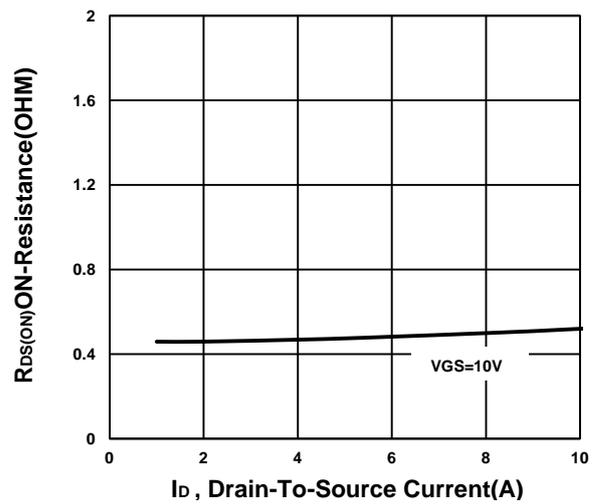
Transfer Characteristics



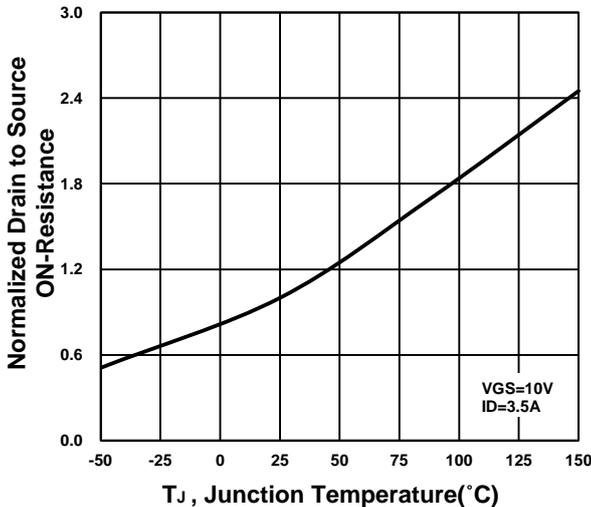
On-Resistance VS Gate-To-Source Voltage



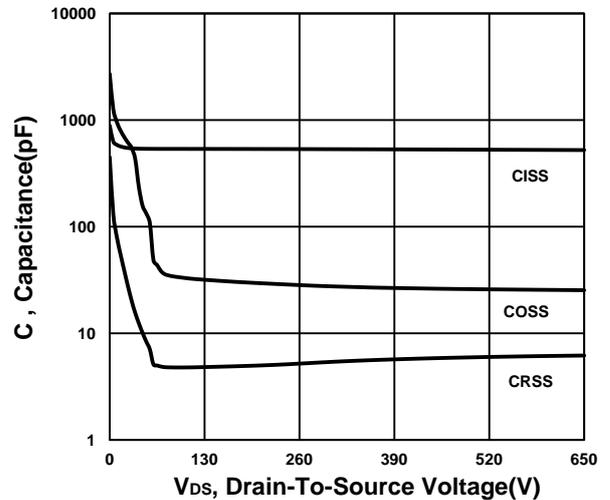
On-Resistance VS Drain Current



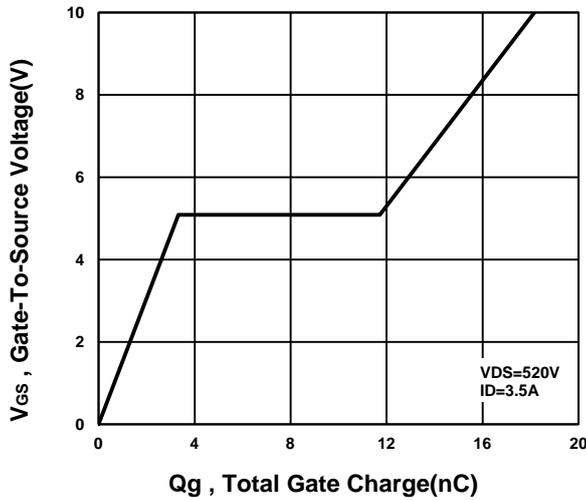
On-Resistance VS Temperature



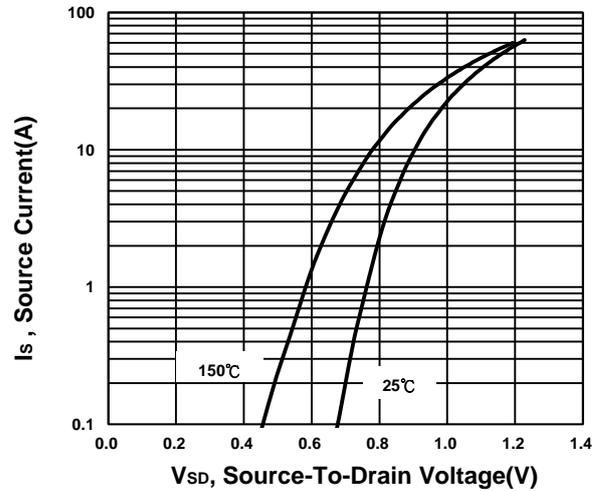
Capacitance Characteristic



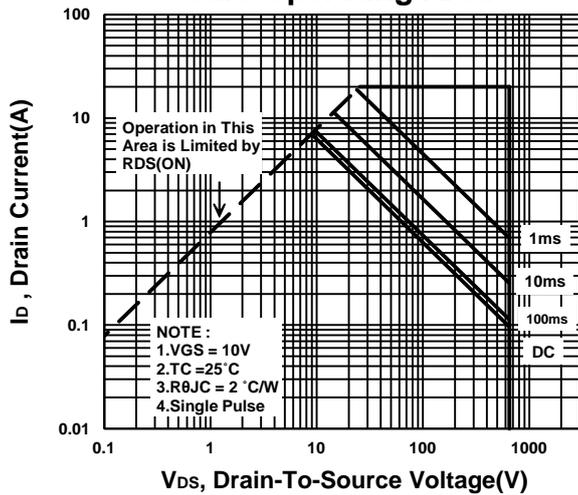
Gate charge Characteristics



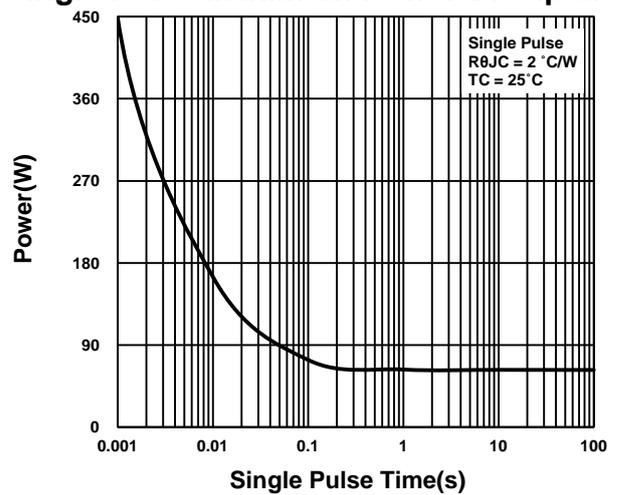
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

