

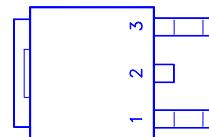
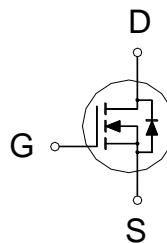
NIKO-SEM
**N-Channel Logic Level Enhancement
Mode Field Effect Transistor**
PP2915AD

TO-252

Halogen-Free & Lead-Free

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
150V	29mΩ	34A


 1.GATE
2.DRAIN
3.SOURCE
ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	I_D	34	A
	$T_C = 100^\circ\text{C}$		24	
Pulsed Drain Current ¹		I_{DM}	111	
Avalanche Current		I_{AS}	8.8	
Avalanche Energy	$L = 1\text{mH}$	E_{AS}	38	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	75	W
	$T_C = 100^\circ\text{C}$		38	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 175	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	°C / W

¹Pulse width limited by maximum junction temperature.
ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	150			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2	2.8	4	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 150\text{V}, V_{GS} = 0\text{V}$			1	μA
		$V_{DS} = 150\text{V}, V_{GS} = 0\text{V}, T_J = 55^\circ\text{C}$			10	

NIKO-SEM
**N-Channel Logic Level Enhancement
Mode Field Effect Transistor**
PP2915AD

TO-252

Halogen-Free & Lead-Free

Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 10V, I _D = 15A	22	29	mΩ
Forward Transconductance ¹	g _{fs}	V _{DS} = 10V, I _D = 15A	15		s
DYNAMIC					
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 75V, f = 1MHz	1223		pF
Output Capacitance	C _{oss}		119		
Reverse Transfer Capacitance	C _{rss}		11		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz	0.5		Ω
Total Gate Charge ²	Q _g	V _{DS} = 75V, V _{GS} = 10V, I _D = 15A	20		nC
Gate-Source Charge ²	Q _{gs}		5.6		
Gate-Drain Charge ²	Q _{gd}		6.5		
Turn-On Delay Time ²	t _{d(on)}	V _{DD} = 75V I _D ≈ 15A, V _{GS} = 10V, R _{GEN} = 6Ω	12		nS
Rise Time ²	t _r		35		
Turn-Off Delay Time ²	t _{d(off)}		21		
Fall Time ²	t _f		40		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)					
Continuous Current	I _S	I _F = 15A, V _{GS} = 0V		34	A
Forward Voltage ¹	V _{SD}			1.2	V
Reverse Recovery Time	t _{rr}		70		nS
Reverse Recovery Charge	Q _{rr}		103		nC

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.²Independent of operating temperature.

NIKO-SEM

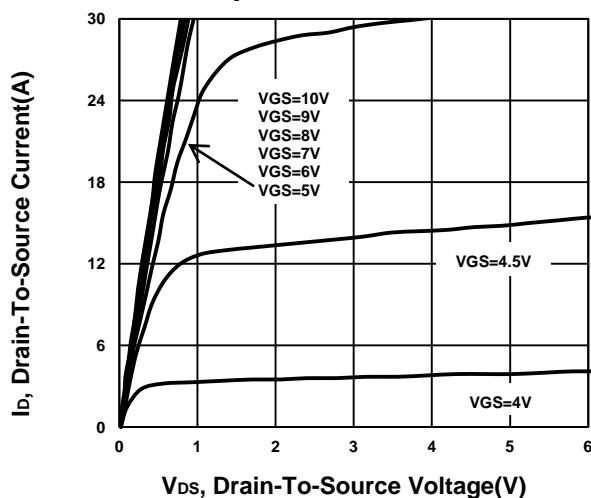
**N-Channel Logic Level Enhancement
Mode Field Effect Transistor**

PP2915AD

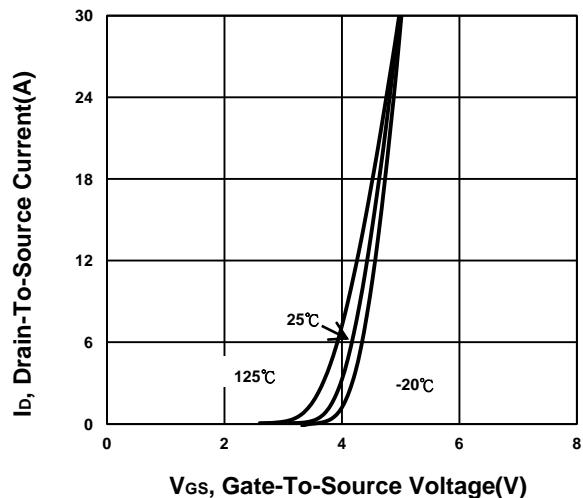
TO-252

Halogen-Free & Lead-Free

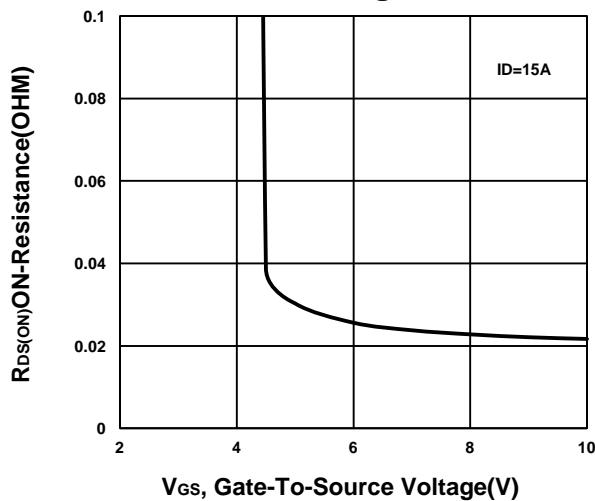
Output Characteristics



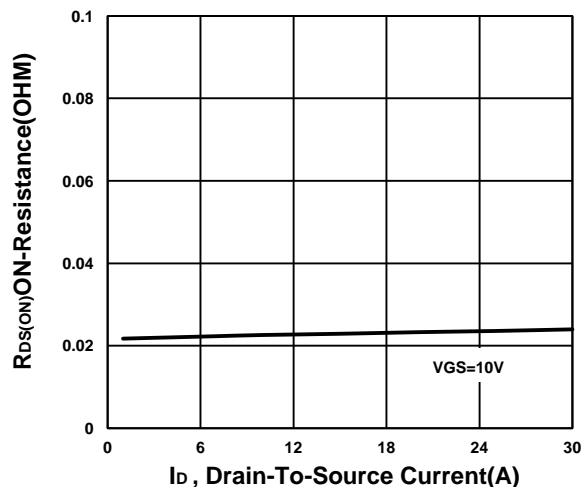
Transfer Characteristics



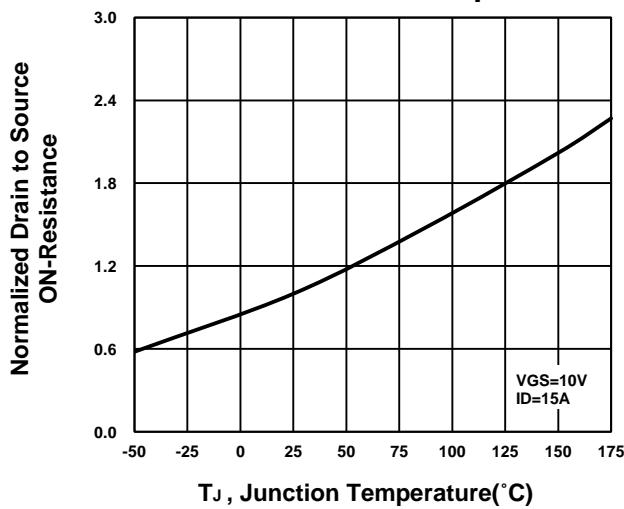
On-Resistance VS Gate-To-Source Voltage



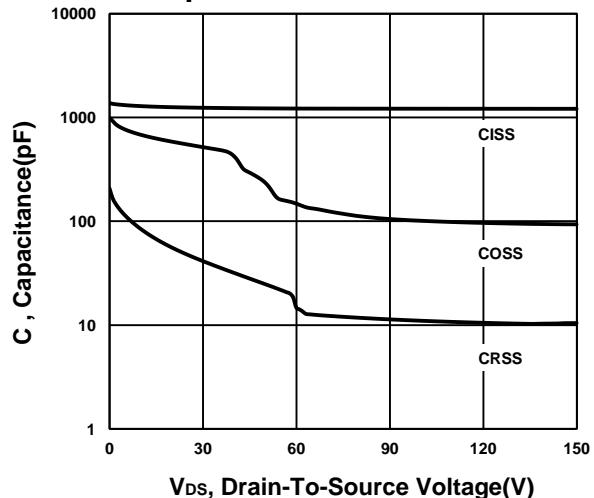
On-Resistance VS Drain Current

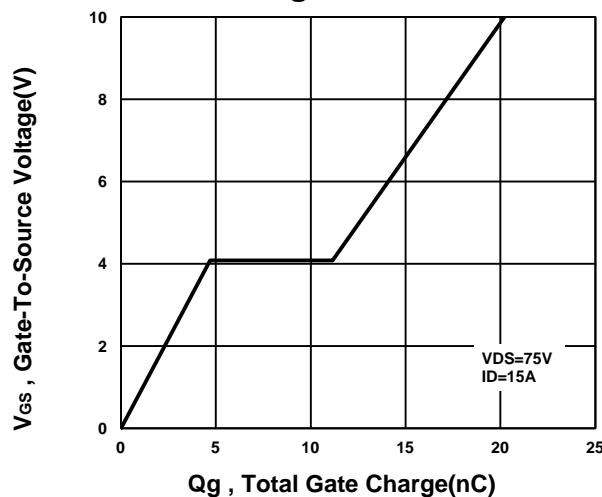
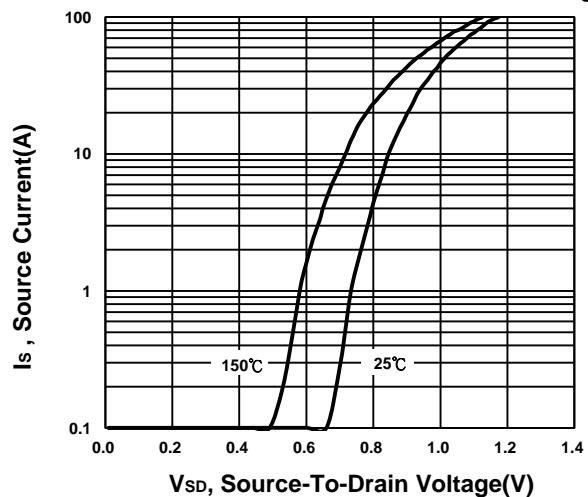
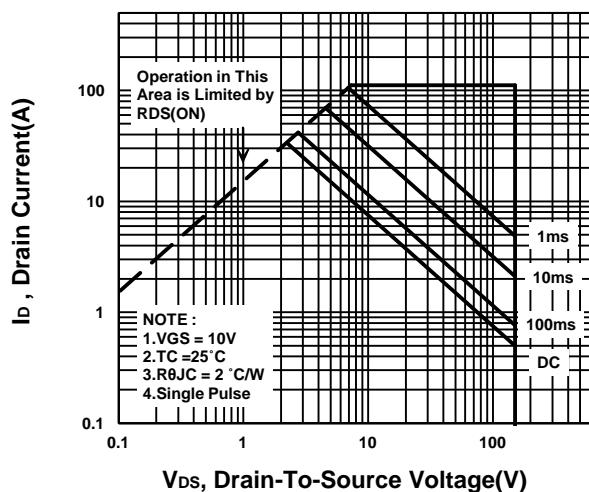
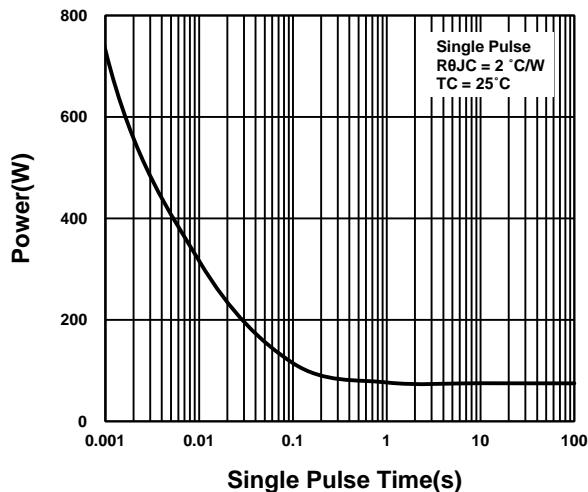


On-Resistance VS Temperature



Capacitance Characteristic



Gate charge Characteristics**Source-Drain Diode Forward Voltage****Safe Operating Area****Single Pulse Maximum Power Dissipation****Transient Thermal Response Curve**