NIKO-SEM

N-Channel Logic Level Enhancement Mode Field Effect Transistor

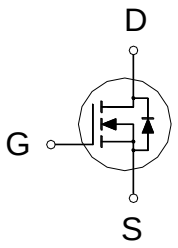
PP4515BD

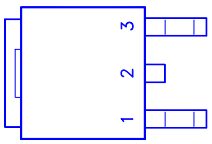
TO-252

Halogen-Free & Lead-Free

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
150V	45mΩ	26A





1.GATE
2.DRAIN
3.SOURCE

RoHS Compliant

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	T_C = 25 °C	I_D	26	A
	T_C = 100 °C		18	
Pulsed Drain Current[1]		I_{DM}	90	
Avalanche Current		I_{AS}	14	
Avalanche Energy	L = 1mH	E_{AS}	74	mJ
Power Dissipation	T_C = 25 °C	P_D	75	W
	T_C = 100 °C		37.5	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 175	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	°C / W

[1]Pulse width limited by maximum junction temperature.

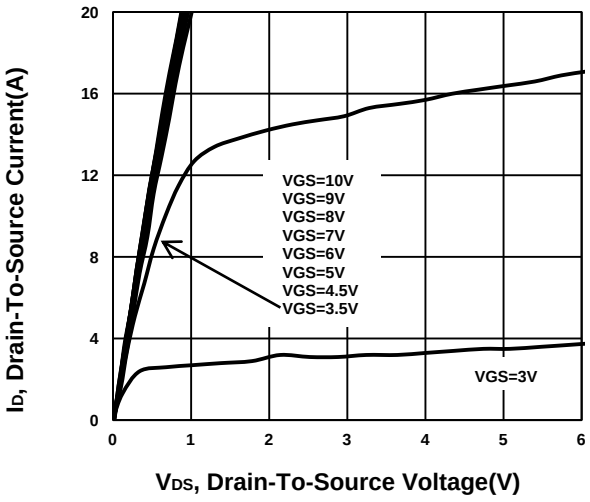
ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	150			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1	2.1	3	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 150V, V_{GS} = 0V			1	μA
		V_{DS} = 150V, V_{GS} = 0V, T_J = 55 °C			10	

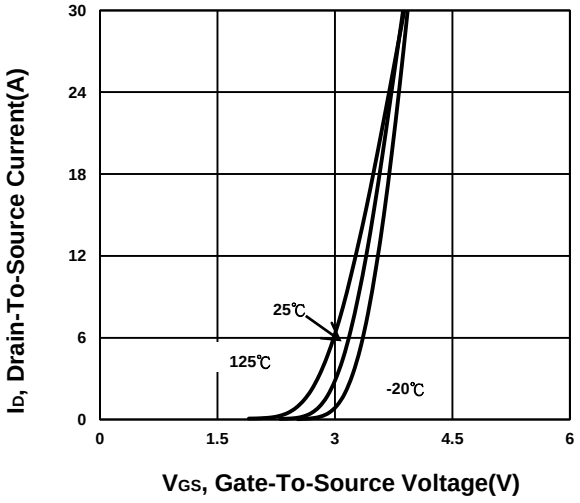
Drain-Source On-State Resistance[1]	$R_{DS(ON)}$	V_{GS} = 10V, I_D = 4A		35	45	mΩ
		V_{GS} = 4.5V, I_D = 4A		40	60	
Forward Transconductance[1]	g_{fs}	V_{DS} = 10V, I_D = 4A		16		S
DYNAMIC						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 75V, f = 1MHz		1169		pF
Output Capacitance	C_{oss}			97		
Reverse Transfer Capacitance	C_{rss}			15		
Gate Resistance	R_g	V_{GS} = 0V, V_{DS} = 0V ,f = 1MHz		0.5		Ω
Total Gate Charge[2]	Q_g	V_{DS} = 75V, V_{GS} = 10V, I_D = 4A		21		nC
Gate-Source Charge[2]	Q_{gs}			3.4		
Gate-Drain Charge[2]	Q_{gd}			6.3		
Turn-On Delay Time[2]	$t_{d(on)}$	V_{DD} = 75V, $I_D \cong$ 4A, V_{GS} = 10V, R_{GEN} = 6Ω		8		nS
Rise Time[2]	t_r			21		
Turn-Off Delay Time[2]	$t_{d(off)}$			23		
Fall Time[2]	t_f			28		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I_S				26	A
Forward Voltage[1]	V_{SD}	I_F = 4A,V_{GS} = 0V			1.2	V
Reverse Recovery Time	t_{rr}	I_F = 4A, dI_F/dt = 100A / μS		67		nS
Reverse Recovery Charge	Q_{rr}			97		nC

[1]Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.
[2]Independent of operating temperature.

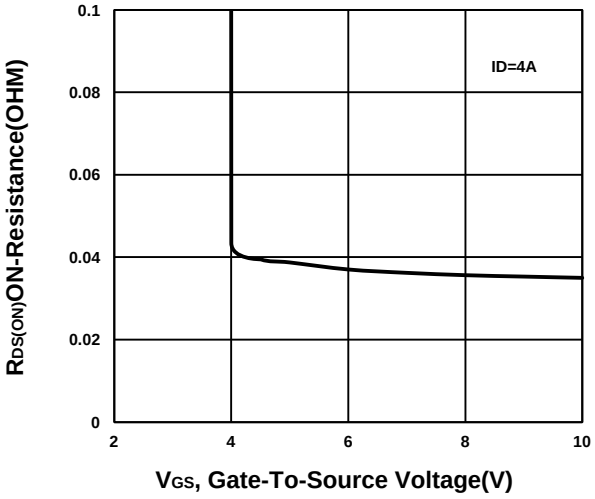
Output Characteristics



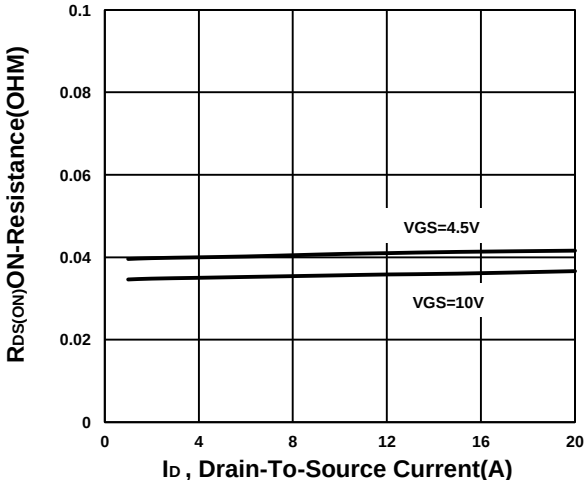
Transfer Characteristics



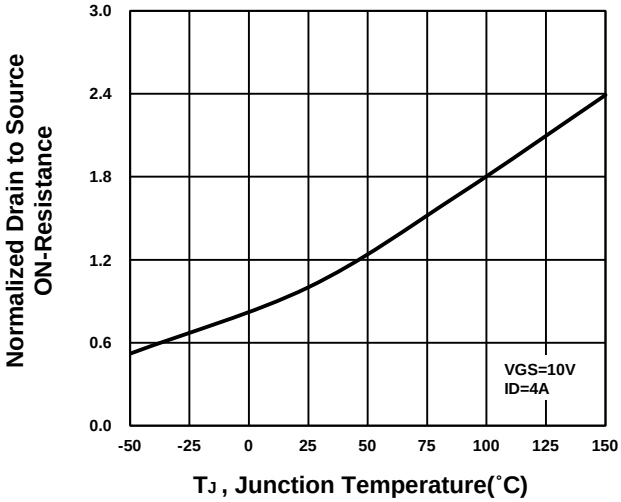
On-Resistance VS Gate-To-Source Voltage



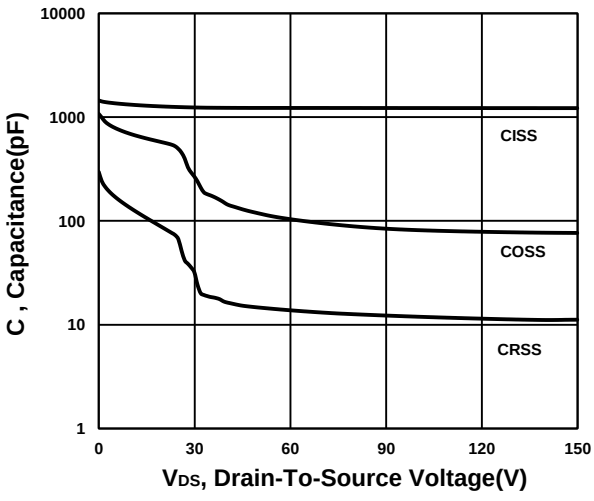
On-Resistance VS Drain Current



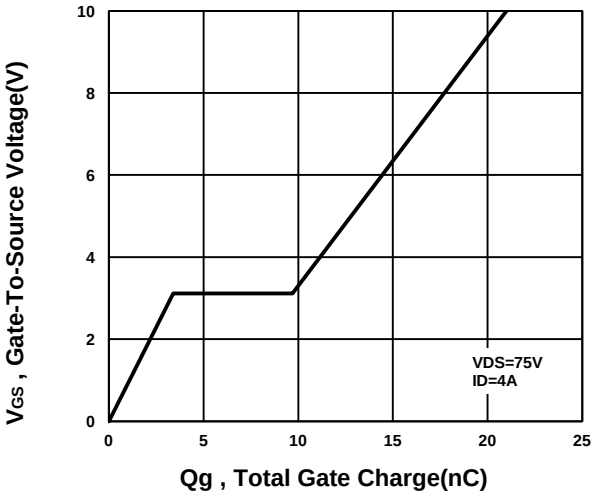
On-Resistance VS Temperature



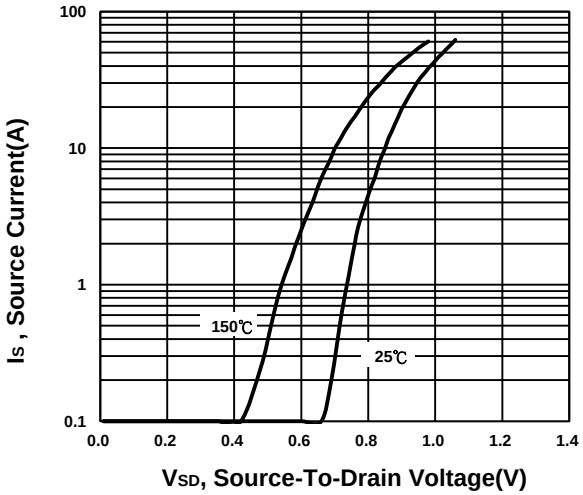
Capacitance Characteristic



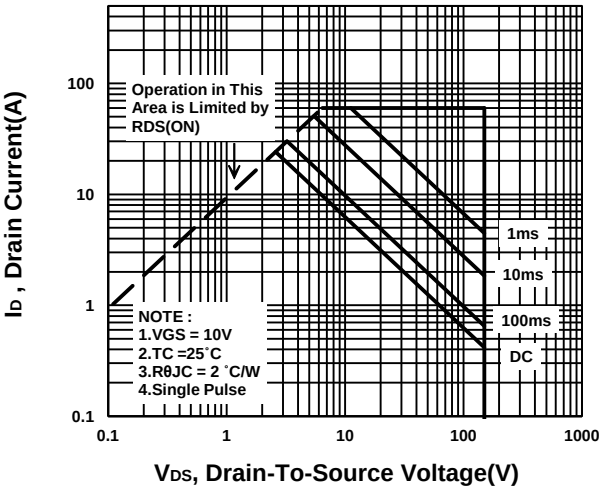
Gate charge Characteristics



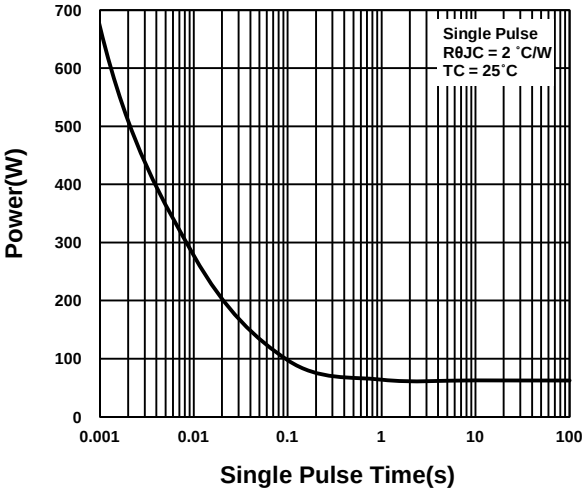
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve