

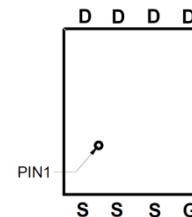
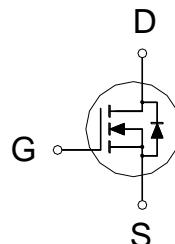
NIKO-SEM**N-Channel Enhancement Mode
Field Effect Transistor****PP1C06AKB**

PDFN 5x6S

Halogen-Free & Lead-Free

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
60V	1.2mΩ	234A



G. GATE
D. DRAIN
S. SOURCE

**ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	I_D	234	A
	$T_C = 100^\circ\text{C}$		148	
Pulsed Drain Current ¹		I_{DM}	378	
Continuous Drain Current	$T_A = 25^\circ\text{C}$	I_D	44	
	$T_A = 70^\circ\text{C}$		35	
Avalanche Current		I_{AS}	92	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	423	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	125	W
	$T_C = 100^\circ\text{C}$		50	
Power Dissipation ³	$T_A = 25^\circ\text{C}$	P_D	4.5	W
	$T_A = 70^\circ\text{C}$		2.9	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$t \leq 10\text{s}$	$R_{\theta JA}$		28	°C / W
Junction-to-Ambient ²	Steady-State	$R_{\theta JA}$		50	
Junction-to-Case	Steady-State	$R_{\theta JC}$		1	

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

³The Power dissipation is based on $R_{\theta JA}$ $t \leq 10\text{s}$ value.

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ C$, Unless Otherwise Noted)

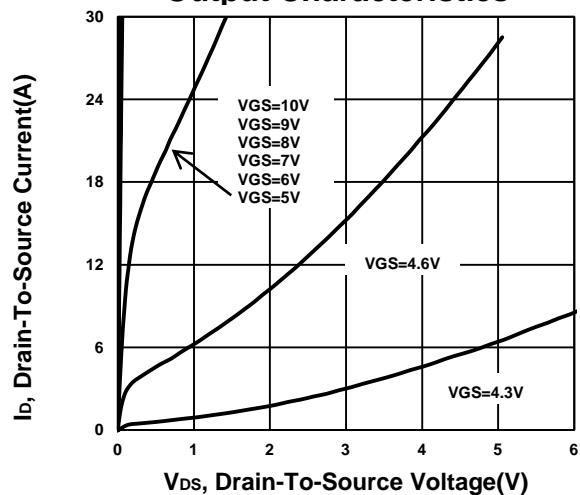
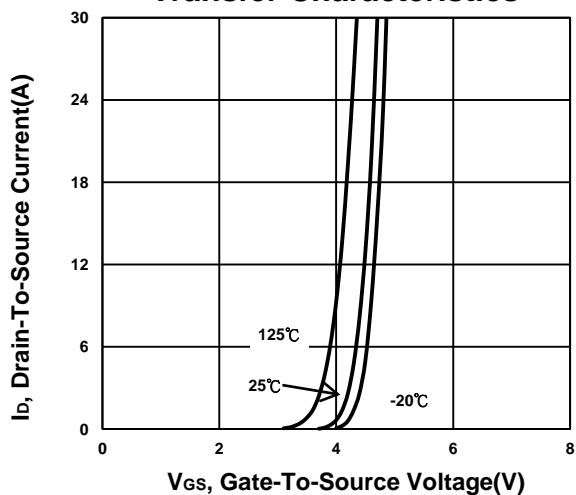
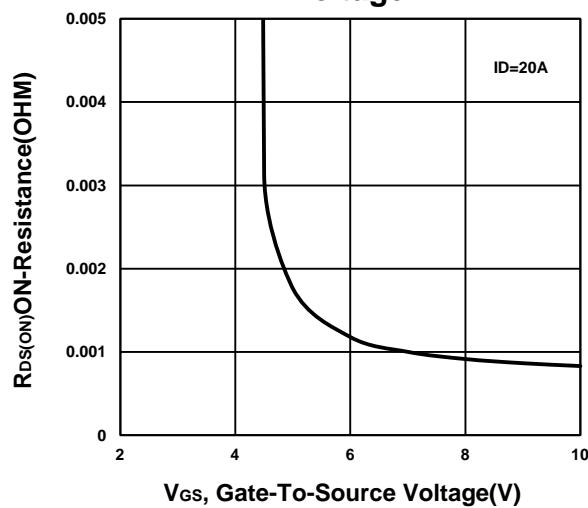
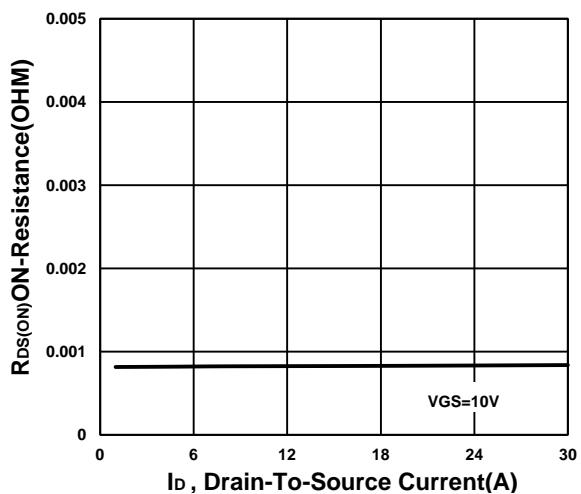
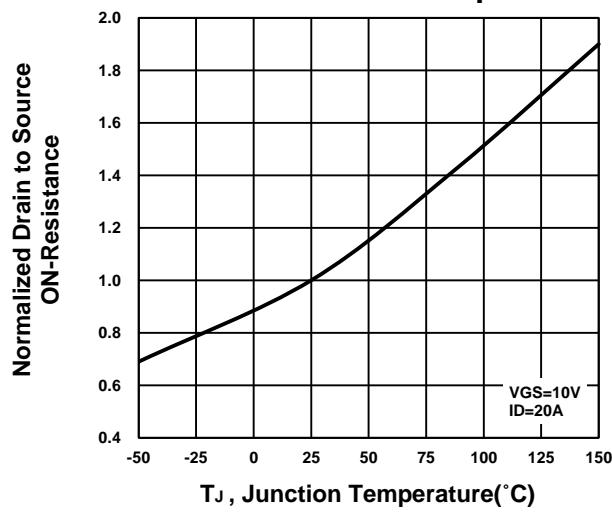
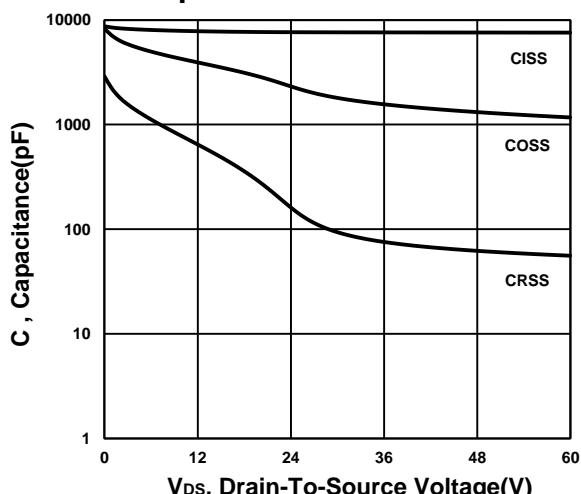
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	60			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2	2.8	4	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 60V, V_{GS} = 0V$			1	
		$V_{DS} = 60V, V_{GS} = 0V, T_J = 55^\circ C$			10	μA
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 20A$		0.83	1.2	$m\Omega$
Forward Transconductance ¹	g_{fs}	$V_{DS} = 5V, I_D = 20A$		98		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 30V, f = 600KHz$		7478		pF
Output Capacitance	C_{oss}			1945		
Reverse Transfer Capacitance	C_{rss}			53		
Gate Resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		1.8		Ω
Total Gate Charge ²	Q_g	$V_{GS} = 10V, V_{DS} = 30V, I_D = 20A$		138		nC
Gate-Source Charge ²	Q_{gs}			34		
Gate-Drain Charge ²	Q_{gd}			38		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DD} = 30V,$ $I_D \approx 20A, V_{GS} = 10V, R_{GEN} = 6\Omega$		36		nS
Rise Time ²	t_r			105		
Turn-Off Delay Time ²	$t_{d(off)}$			130		
Fall Time ²	t_f			116		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ C$)						
Continuous Current	I_S				104	A
Forward Voltage ¹	V_{SD}	$I_F = 20A, V_{GS} = 0V$			1.2	V
Reverse Recovery Time	t_{rr}	$I_F = 20A, dI_F/dt = 100A/\mu s$		67		nS
Reverse Recovery Charge	Q_{rr}			106		nC

¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.²Independent of operating temperature.

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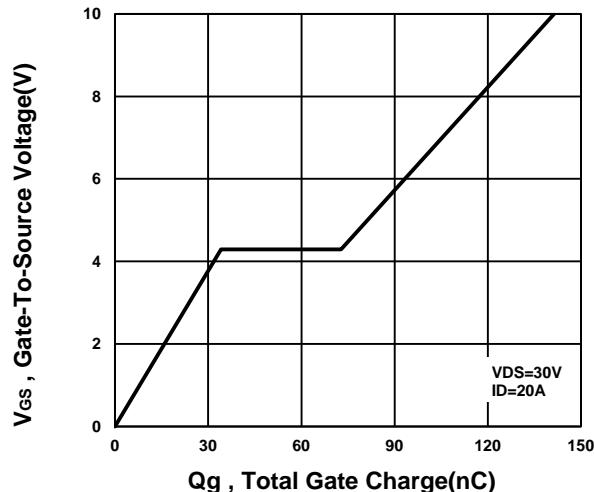
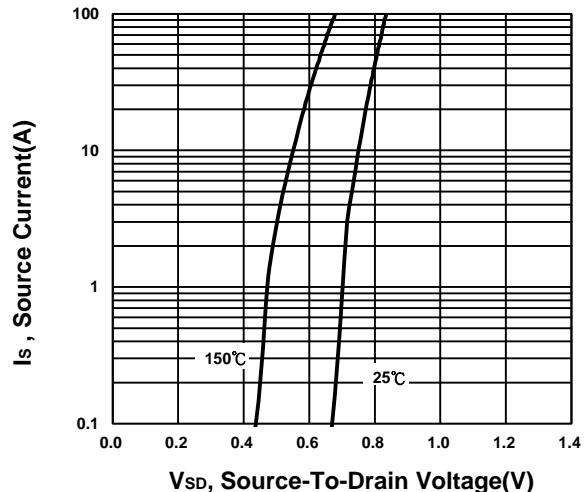
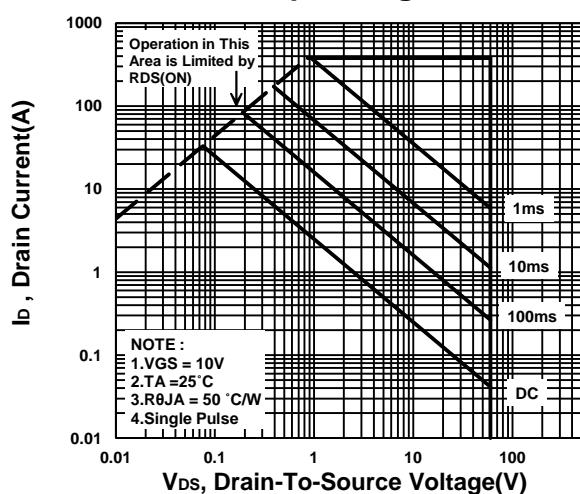
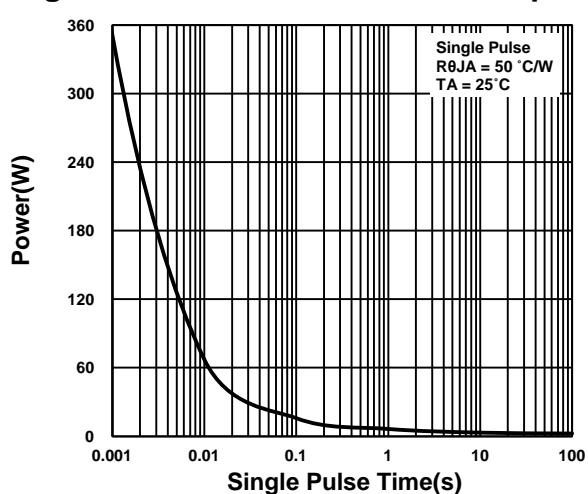
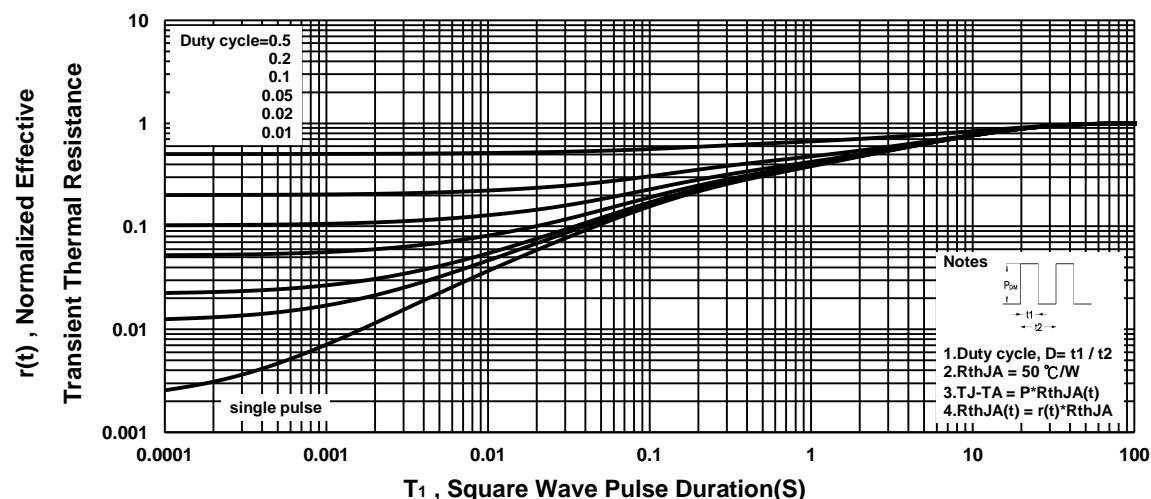
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Output Characteristics**Transfer Characteristics****On-Resistance VS Gate-To-Source Voltage****On-Resistance VS Drain Current****On-Resistance VS Temperature****Capacitance Characteristic**

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Gate charge Characteristics**Source-Drain Diode Forward Voltage****Safe Operating Area****Single Pulse Maximum Power Dissipation****Transient Thermal Response Curve**

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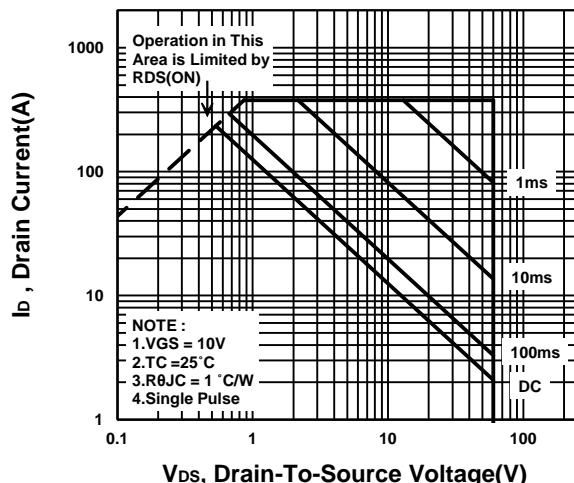
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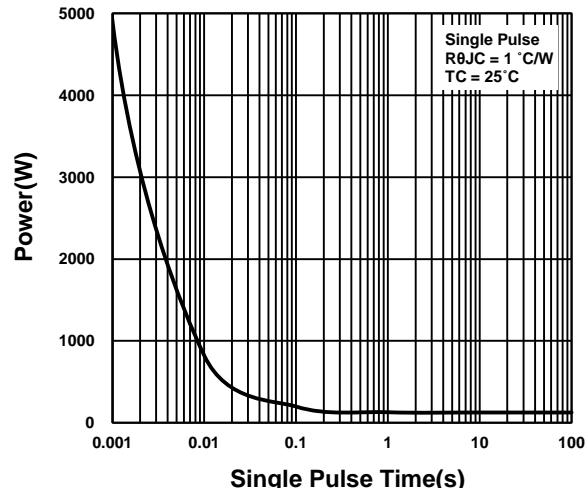
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Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

