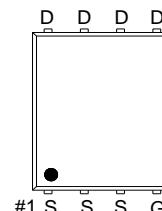
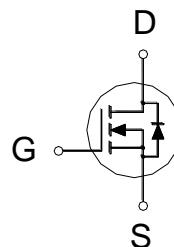


NIKO-SEM
**N-Channel Enhancement Mode
Field Effect Transistor**
PP9H06BK
PDFN 5x6P
Halogen-Free & Lead-Free
PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
60V	9.1mΩ	60A


G. GATE
D. DRAIN
S. SOURCE
ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	I_D	60	A
	$T_C = 100^\circ\text{C}$		42	
Pulsed Drain Current ¹		I_{DM}	80	
Continuous Drain Current	$T_A = 25^\circ\text{C}$	I_D	11.8	
	$T_A = 70^\circ\text{C}$		9.8	
Avalanche Current		I_{AS}	27	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	36	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	71	W
	$T_C = 100^\circ\text{C}$		35	
Power Dissipation ³	$T_A = 25^\circ\text{C}$	P_D	2.7	W
	$T_A = 70^\circ\text{C}$		1.9	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 175	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$t \leq 10\text{s}$	$R_{\theta JA}$	$^{\circ}\text{C} / \text{W}$	55	$^{\circ}\text{C} / \text{W}$
Junction-to-Ambient ²	Steady-State	$R_{\theta JA}$		75	
Junction-to-Case	Steady-State	$R_{\theta JC}$		2.1	

¹Pulse width limited by maximum junction temperature.²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.³The Power dissipation is based on $R_{\theta JA}$ $t \leq 10\text{s}$ value.

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	60			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1	1.8	3	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 60\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
		$V_{\text{DS}} = 60\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 55^\circ\text{C}$			10	
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		7	9.1	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 20\text{A}$		11.5	14.5	
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 5\text{V}, I_D = 20\text{A}$		41		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 30\text{V}, f = 1\text{MHz}$		1095		pF
Output Capacitance	C_{oss}			553		
Reverse Transfer Capacitance	C_{rss}			25		
Gate Resistance	R_g	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		0.9		Ω
Total Gate Charge ²	Q_g	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		18		nC
				9.5		
Gate-Source Charge ²	Q_{gs}			3		
Gate-Drain Charge ²	Q_{gd}			4.5		
Turn-On Delay Time ²	$t_{\text{d}(\text{on})}$	$V_{\text{DS}} = 30\text{V}, I_D \approx 20\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		9.5		nS
Rise Time ²	t_r			5.3		
Turn-Off Delay Time ²	$t_{\text{d}(\text{off})}$			24		
Fall Time ²	t_f			28		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current	I_S	$I_F = 20\text{A}, V_{\text{GS}} = 0\text{V}$			59	A
Forward Voltage ¹	V_{SD}				1.2	V
Reverse Recovery Time	t_{rr}				31	nS
Reverse Recovery Charge	Q_{rr}				20	nC

¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.

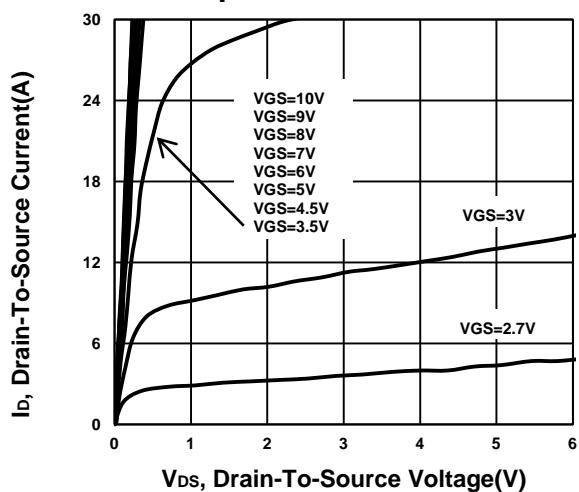
²Independent of operating temperature.

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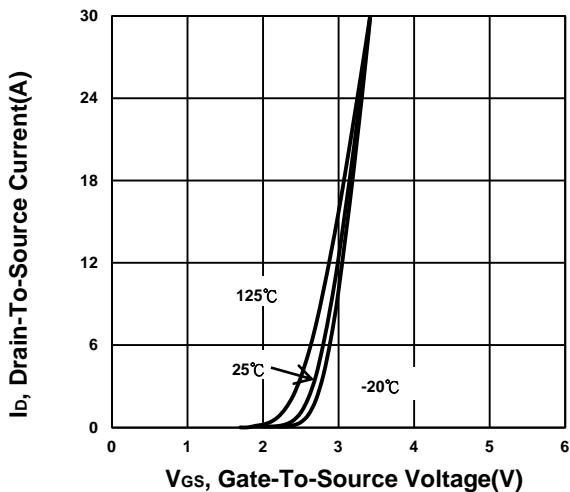
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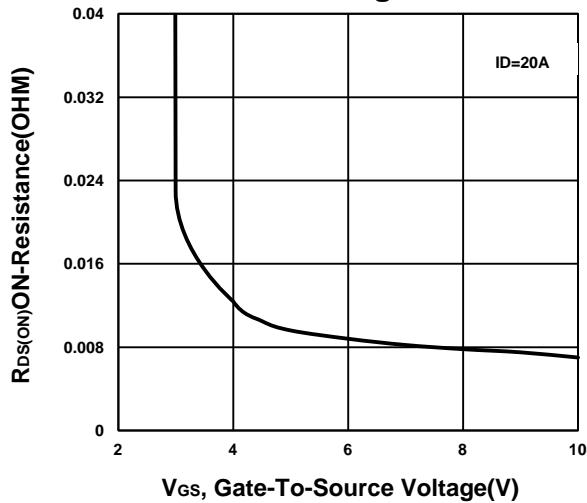
Output Characteristics



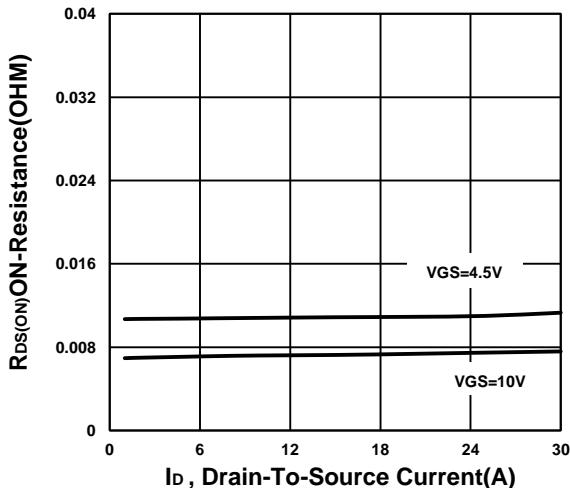
Transfer Characteristics



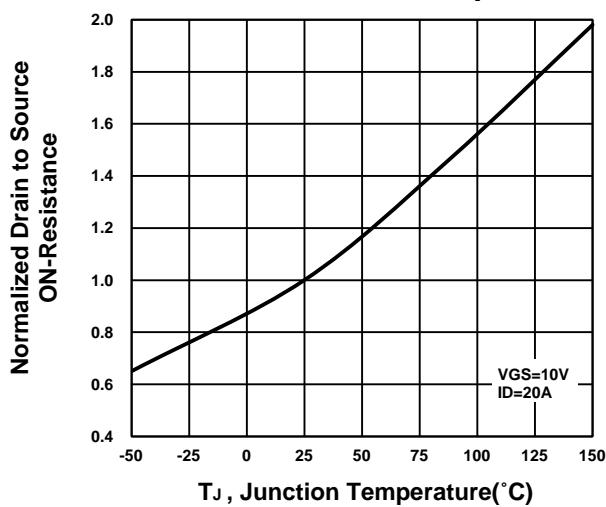
On-Resistance VS Gate-To-Source Voltage



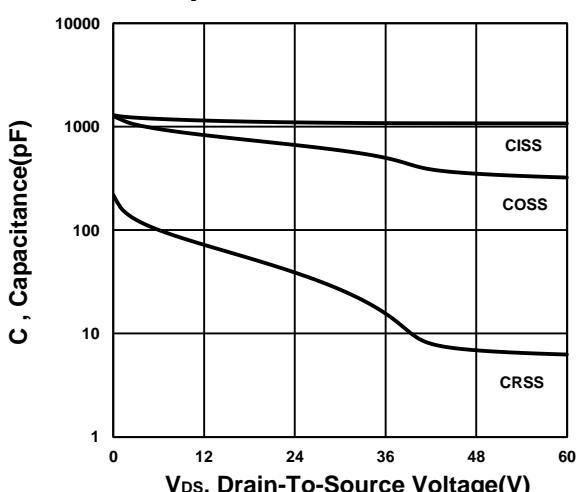
On-Resistance VS Drain Current

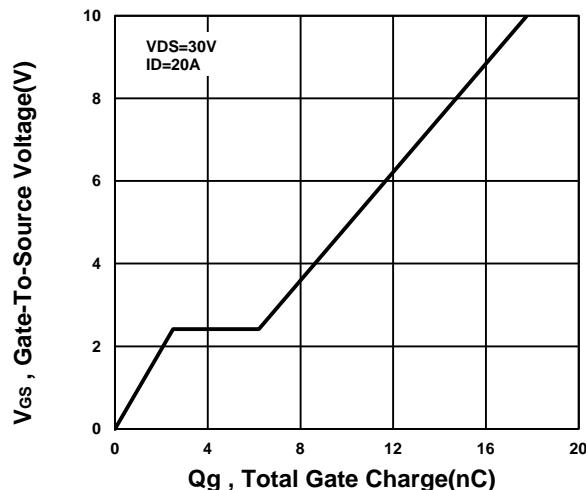
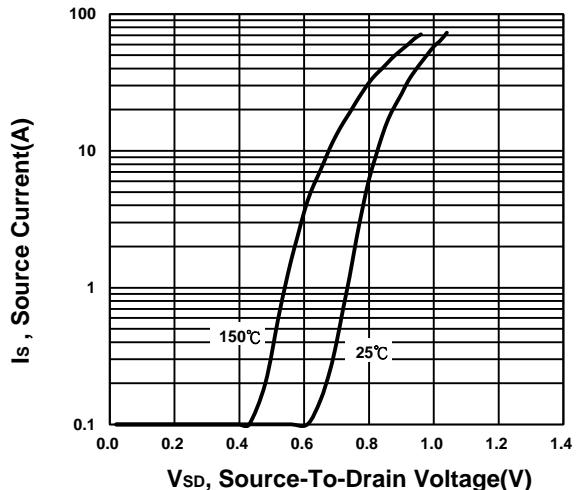
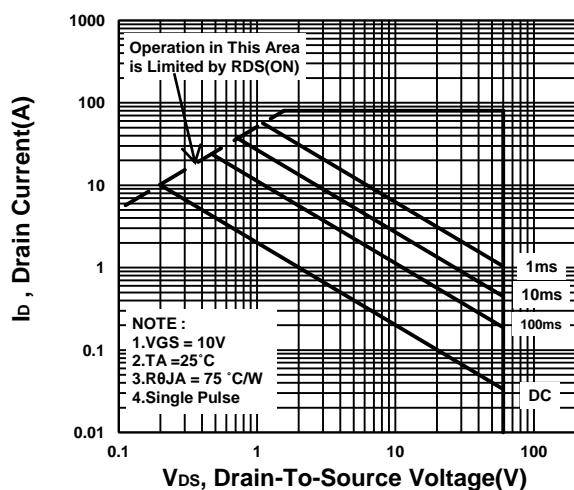
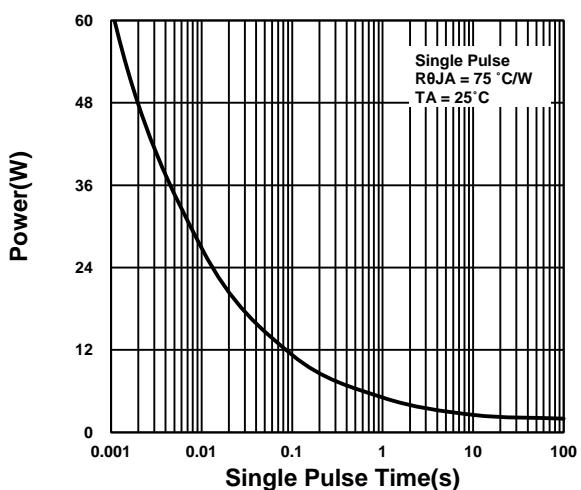
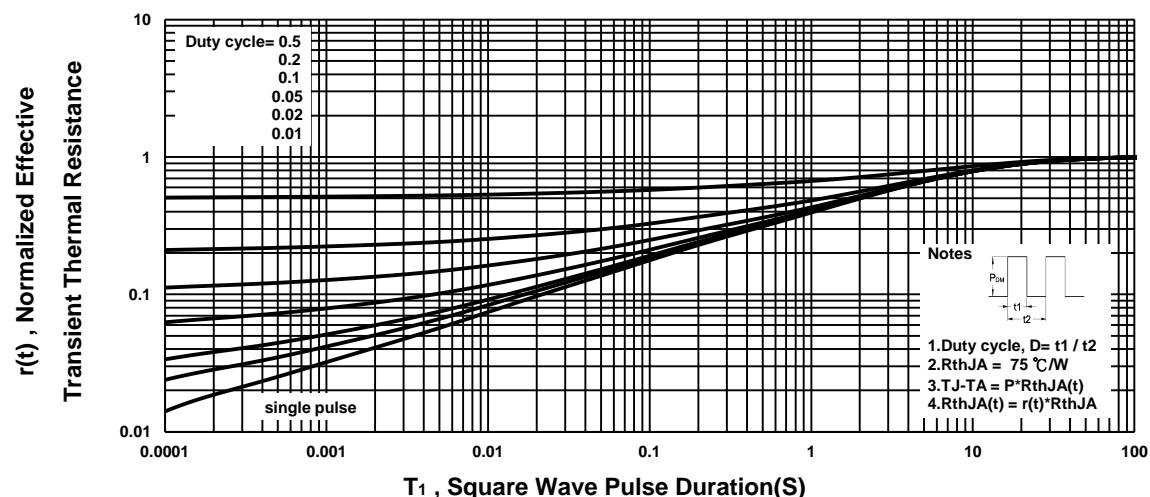


On-Resistance VS Temperature



Capacitance Characteristic



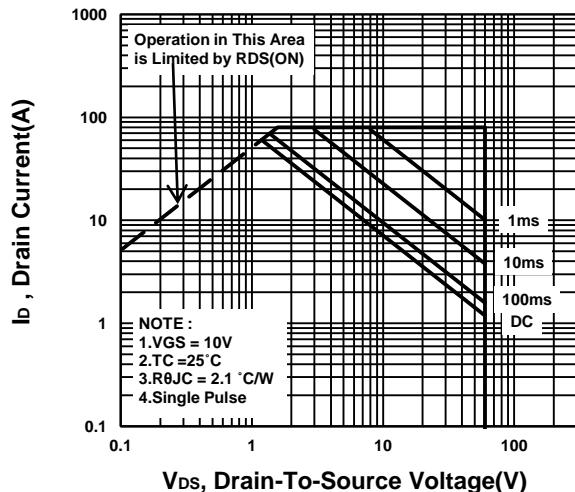
NIKO-SEM**N-Channel Enhancement Mode
Field Effect Transistor****PP9H06BK
PDFN 5x6P
Halogen-Free & Lead-Free****Gate charge Characteristics****Source-Drain Diode Forward Voltage****Safe Operating Area****Single Pulse Maximum Power Dissipation****Transient Thermal Response Curve**

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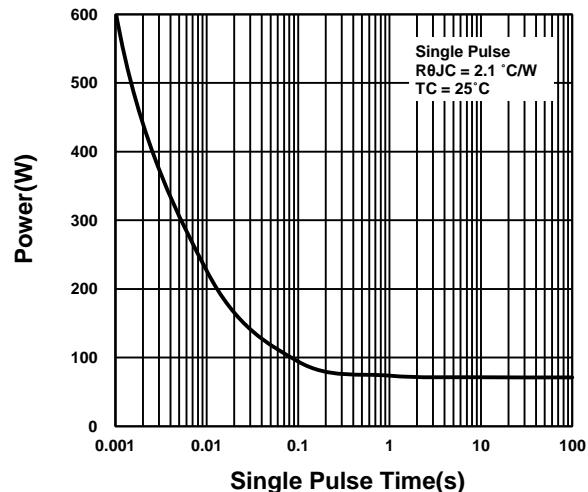
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Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

