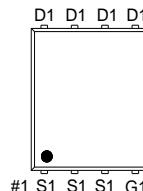
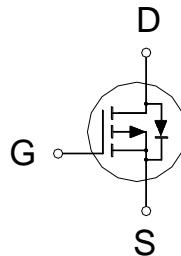


**NIKO-SEM****P-Channel Logic Level Enhancement Mode  
Field Effect Transistor****PK555BA**  
PDFN 5x6P  
Halogen-Free & Lead-Free**PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
-30V	28mΩ	-19A



G : GATE  
D : DRAIN  
S : SOURCE

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	-19	A
		-12	
		-7.8	
		-6.2	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	-50	
Avalanche Current	$I_{AS}$	-19.3	
Avalanche Energy	$E_{AS}$	18.6	mJ
Power Dissipation <sup>3</sup>	$P_D$	19	W
		7.9	
		3.1	
		2	
Operating Junction & Storage Temperature Range	$T_j, T_{stg}$	-55 to 150	°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient <sup>2</sup>	$R_{\theta JA}$		40	°C / W
	$R_{\theta JA}$		63	
Junction-to-Case	$R_{\theta JC}$		6.3	

<sup>1</sup>Pulse width limited by maximum junction temperature.

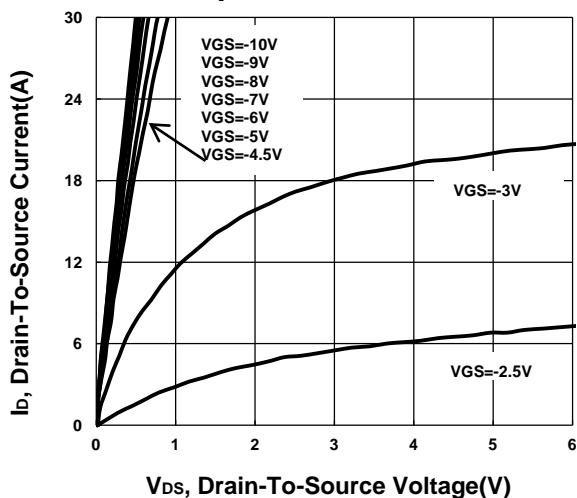
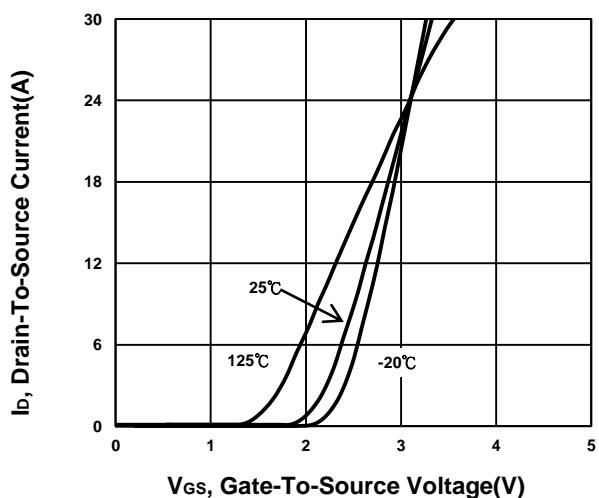
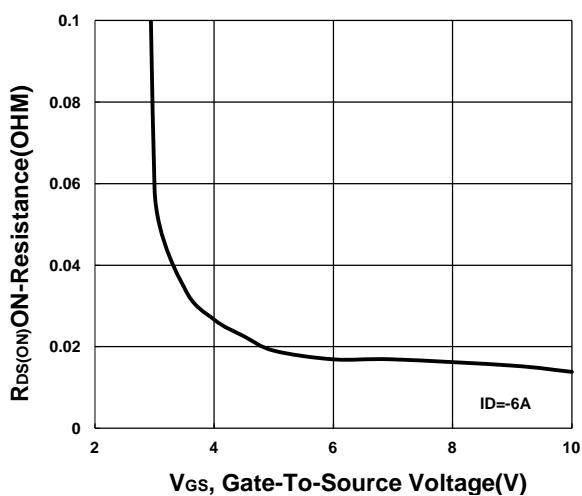
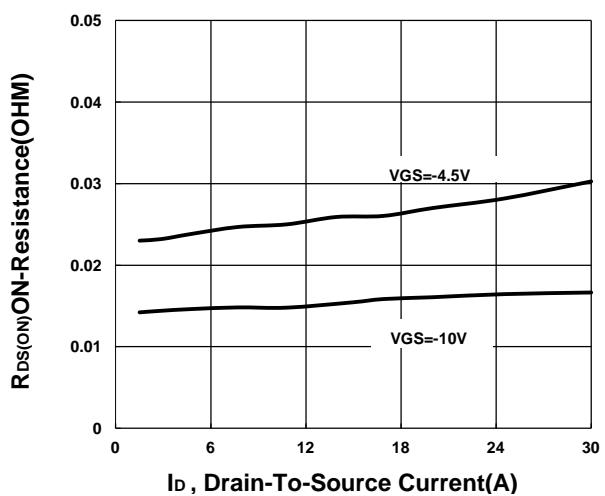
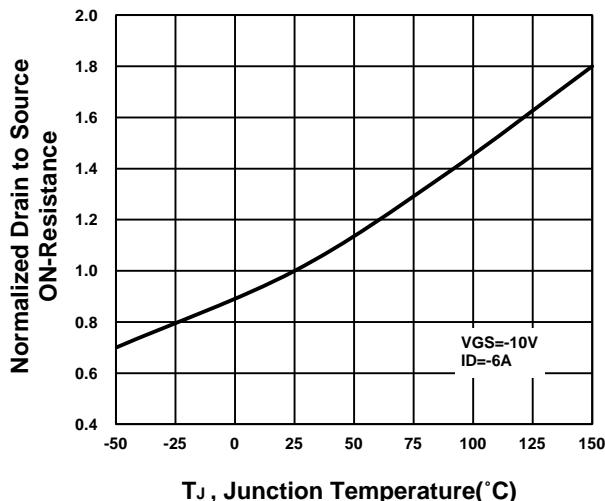
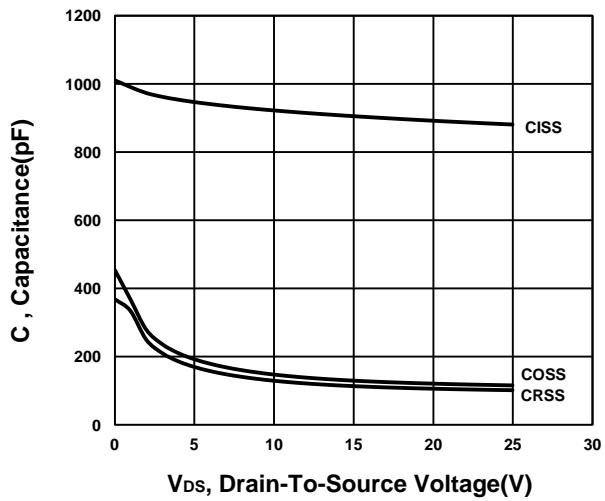
<sup>2</sup>The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ . The value in any given application depends on the user's specific board design.

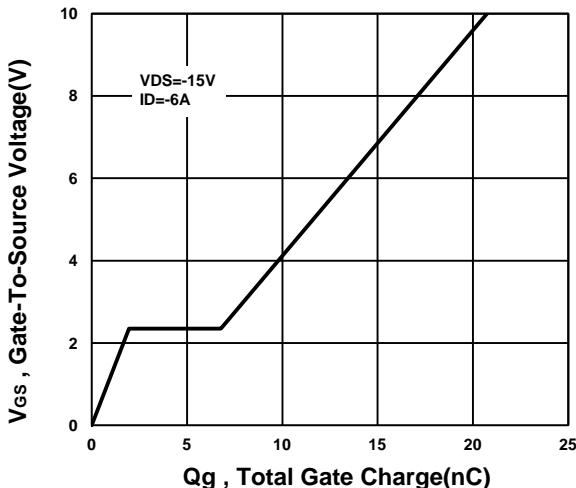
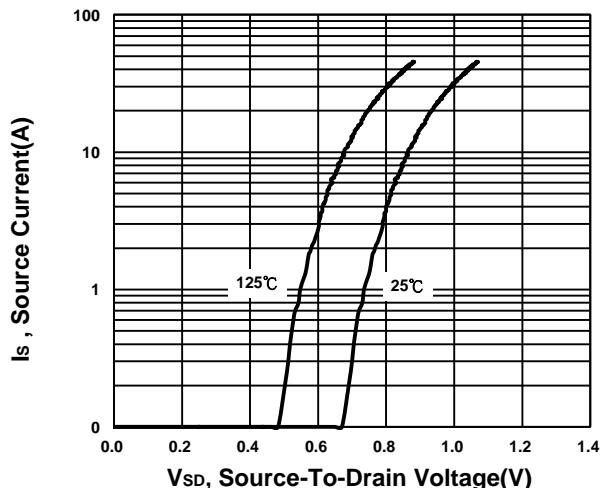
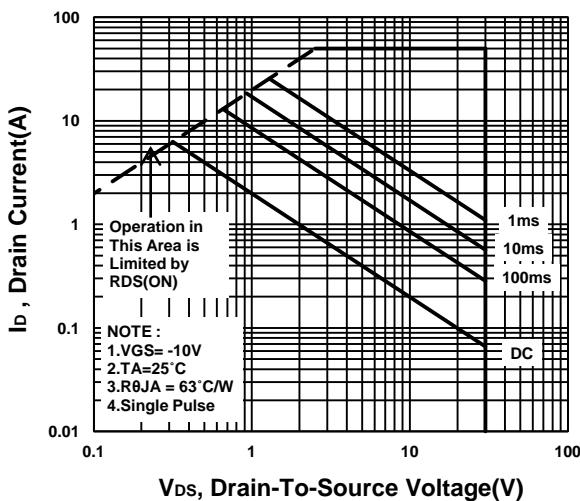
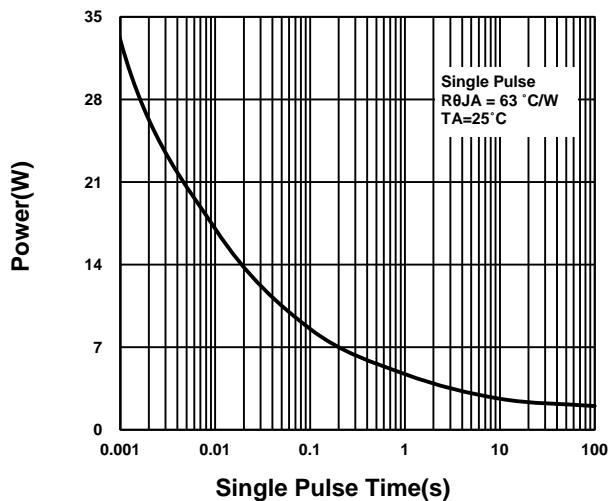
<sup>3</sup>The Power dissipation is based on  $R_{\theta JA}$  t  $\leq 10\text{s}$  value.

**NIKO-SEM****P-Channel Logic Level Enhancement Mode  
Field Effect Transistor****PK555BA  
PDFN 5x6P  
Halogen-Free & Lead-Free****ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	-30			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-0.8	-1.5	-2.5	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -24V, V <sub>GS</sub> = 0V			-1	uA
		V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125 °C			-10	
Drain-Source On-State Resistance <sup>1</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -6A		26	45	mΩ
		V <sub>GS</sub> = -10V, I <sub>D</sub> = -6A		16	28	
Forward Transconductance <sup>1</sup>	g <sub>f</sub>	V <sub>DS</sub> = -5V, I <sub>D</sub> = -6A		22		S
<b>DYNAMIC</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = -15V, f = 1MHz	770	963	1156	pF
Output Capacitance	C <sub>oss</sub>		107	134	161	
Reverse Transfer Capacitance	C <sub>rss</sub>		70	118	165	
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1MHz	5	10	20	Ω
Total Gate Charge <sup>2</sup>	Q <sub>g(VGS=-10V)</sub>	V <sub>DS</sub> = -15V, I <sub>D</sub> = -6A	17	22	26.4	nC
	Q <sub>g(VGS=-4.5V)</sub>		8.6	10.8	13	
Gate-Source Charge <sup>2</sup>	Q <sub>gs</sub>		1.9	2.4	3	
Gate-Drain Charge <sup>2</sup>	Q <sub>gd</sub>		3.2	5.4	7.6	
Turn-On Delay Time <sup>2</sup>	t <sub>d(on)</sub>	V <sub>DS</sub> = -15V, I <sub>D</sub> ≈ -6A, V <sub>GS</sub> = -10V, R <sub>GS</sub> = 6Ω		16		nS
Rise Time <sup>2</sup>	t <sub>r</sub>			18		
Turn-Off Delay Time <sup>2</sup>	t <sub>d(off)</sub>			40		
Fall Time <sup>2</sup>	t <sub>f</sub>			26		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T<sub>J</sub> = 25 °C)</b>						
Continuous Current	I <sub>s</sub>	I <sub>F</sub> = -6A, V <sub>GS</sub> = 0V			-20	A
Forward Voltage <sup>1</sup>	V <sub>SD</sub>				-1	V
Reverse Recovery Time	t <sub>rr</sub>			11		nS
Reverse Recovery Charge	Q <sub>rr</sub>			3.3		nC

<sup>1</sup>Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.<sup>2</sup>Independent of operating temperature.

**Output Characteristics****Transfer Characteristics****On-Resistance VS Gate-to-Source****On-Resistance VS Drain Current****On-Resistance VS Temperature****Capacitance Characteristic**

**NIKO-SEM****P-Channel Logic Level Enhancement Mode  
Field Effect Transistor****PK555BA  
PDFN 5x6P  
Halogen-Free & Lead-Free****Gate charge Characteristics****Source-Drain Diode Forward Voltage****Safe Operating Area****Single Pulse Maximum Power Dissipation****Transient Thermal Response Curve**