

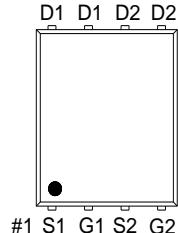
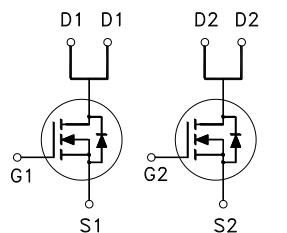
**NIKO-SEM**
**Dual N-Channel Enhancement Mode  
Field Effect Transistor**
**PK6M6DX**

PDFN 5x6P

Halogen-Free &amp; Lead-Free

**PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
40V	8.5mΩ	45A


G. GATE  
D. DRAIN  
S. SOURCE
**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		$V_{DS}$	40	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	$I_D$	45	A
	$T_C = 100^\circ\text{C}$		28.8	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	94	
Continuous Drain Current	$T_A = 25^\circ\text{C}$	$I_D$	14	A
	$T_A = 70^\circ\text{C}$		11	
Avalanche Current		$I_{AS}$	32	
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	51	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	35	W
	$T_C = 100^\circ\text{C}$		14	
Power Dissipation	$T_A = 25^\circ\text{C}$	$P_D$	3.1	W
	$T_A = 70^\circ\text{C}$		2	
Operating Junction & Storage Temperature Range		$T_j, T_{stg}$	-55 to 150	°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient <sup>2</sup>	$t \leq 10\text{s}$	$R_{\theta JA}$		40	°C / W
	Steady-State			76	
Junction-to-Case		$R_{\theta JC}$		3.6	

<sup>1</sup>Pulse width limited by maximum junction temperature.<sup>2</sup>The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ .

**NIKO-SEM**
**Dual N-Channel Enhancement Mode  
Field Effect Transistor**
**PK6M6DX**

PDFN 5x6P

Halogen-Free &amp; Lead-Free

**ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	40			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.3	1.8	2.3	
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 32\text{V}, V_{\text{GS}} = 0\text{V}$			1	
		$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 55^\circ\text{C}$			10	$\mu\text{A}$
Drain-Source On-State Resistance <sup>1</sup>	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 11\text{A}$		7.5	12	
		$V_{\text{GS}} = 10\text{V}, I_D = 11\text{A}$		6.2	8.5	$\text{m}\Omega$
Forward Transconductance <sup>1</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = 5\text{V}, I_D = 11\text{A}$		80		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 20\text{V}, f = 1\text{MHz}$		2004		pF
Output Capacitance	$C_{\text{oss}}$			231		
Reverse Transfer Capacitance	$C_{\text{rss}}$			147		
Gate Resistance	$R_g$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		1.1		$\Omega$
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{\text{GS}} = 10\text{V}$		37		nC
				19		
Gate-Source Charge <sup>2</sup>	$Q_{\text{gs}}$	$V_{\text{GS}} = 4.5\text{V}$		5.4		nC
Gate-Drain Charge <sup>2</sup>	$Q_{\text{gd}}$			7.9		
Turn-On Delay Time <sup>2</sup>	$t_{\text{d}(\text{on})}$	$V_{\text{DS}} = 20\text{V}, I_D \approx 11\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		17		nS
Rise Time <sup>2</sup>	$t_r$			58		
Turn-Off Delay Time <sup>2</sup>	$t_{\text{d}(\text{off})}$			54		
Fall Time <sup>2</sup>	$t_f$			88		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ\text{C}</math>)</b>						
Continuous Current	$I_S$	$I_F = 11\text{A}, V_{\text{GS}} = 0\text{V}$			27	A
Forward Voltage <sup>1</sup>	$V_{\text{SD}}$				1.3	V
Reverse Recovery Time	$t_{\text{rr}}$			21		nS
Reverse Recovery Charge	$Q_{\text{rr}}$			11		nC

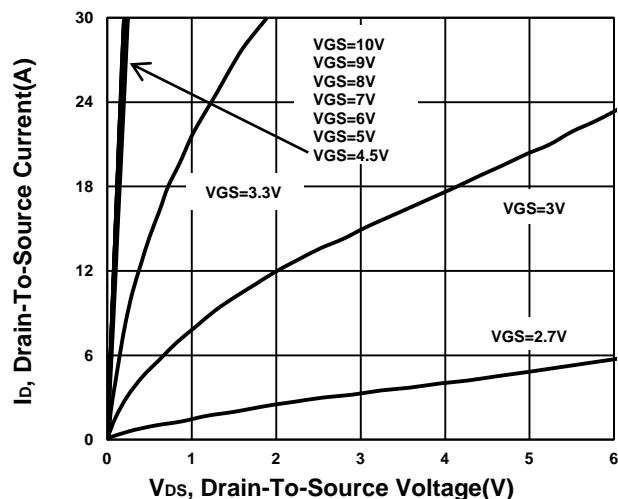
<sup>1</sup>Pulse test : Pulse Width  $\leq 300\ \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .<sup>2</sup>Independent of operating temperature.

**NIKO-SEM**

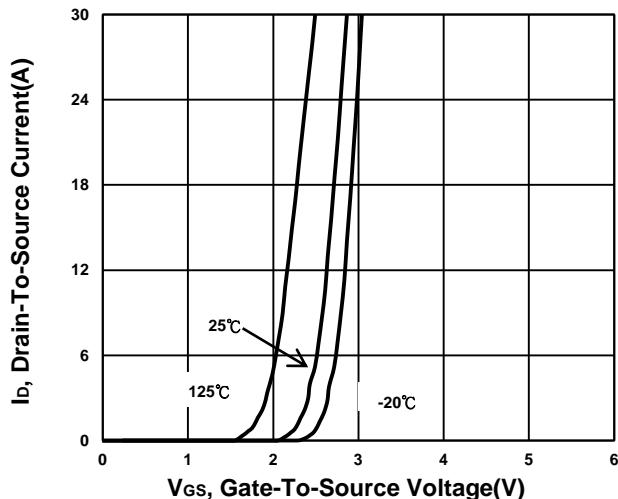
**Dual N-Channel Enhancement Mode  
Field Effect Transistor**

**PK6M6DX**  
**PDFN 5x6P**  
**Halogen-Free & Lead-Free**

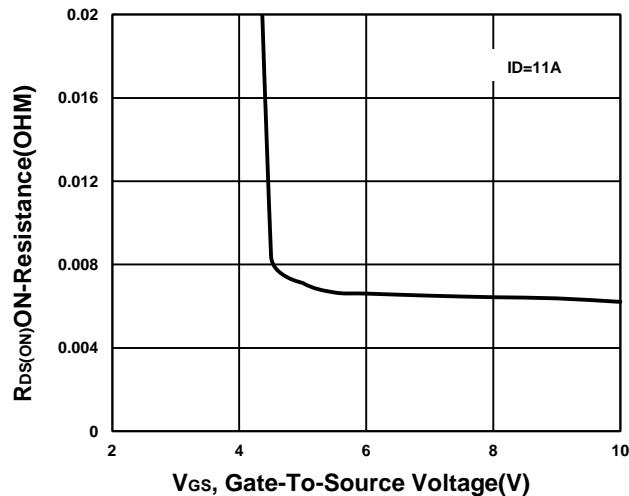
**Output Characteristics**



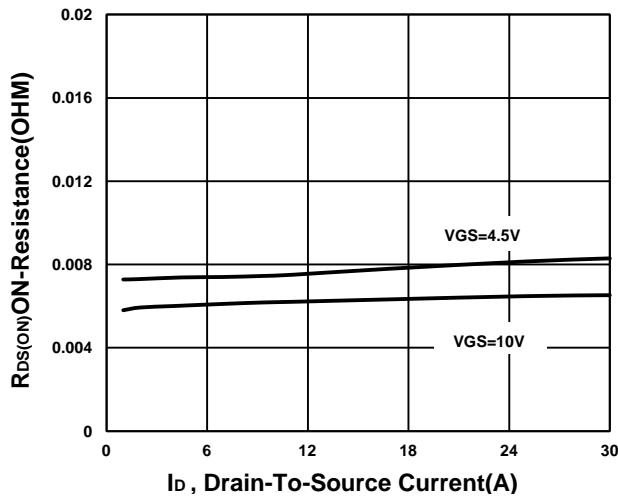
**Transfer Characteristics**



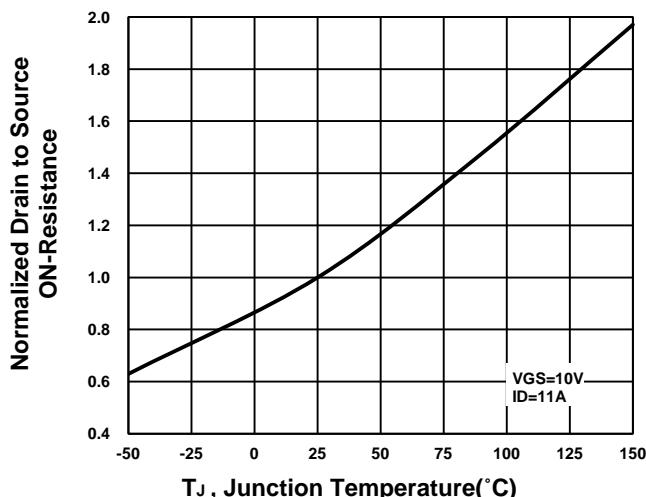
**On-Resistance VS Gate-To-Source Voltage**



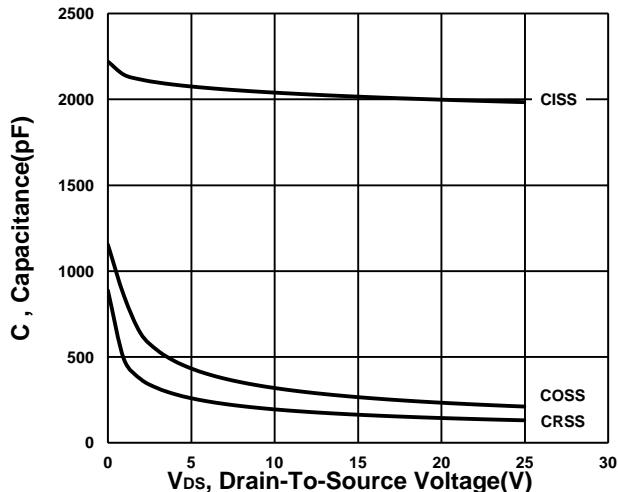
**On-Resistance VS Drain Current**

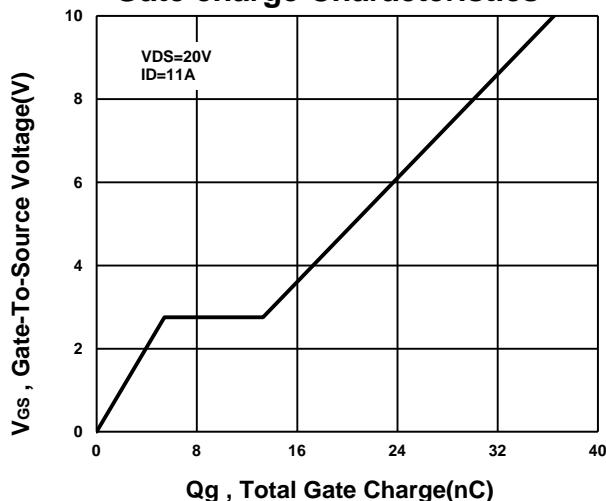
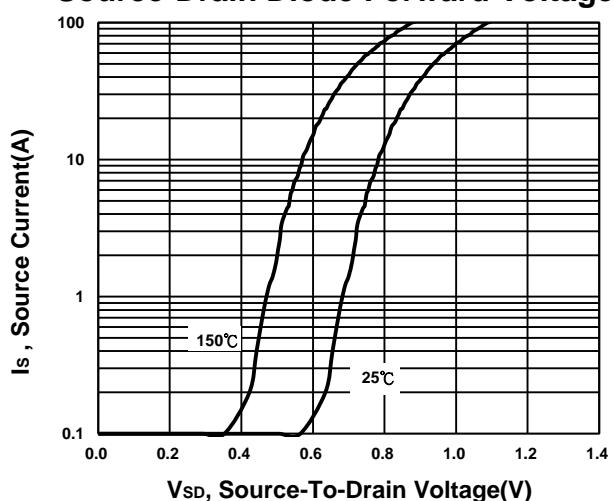
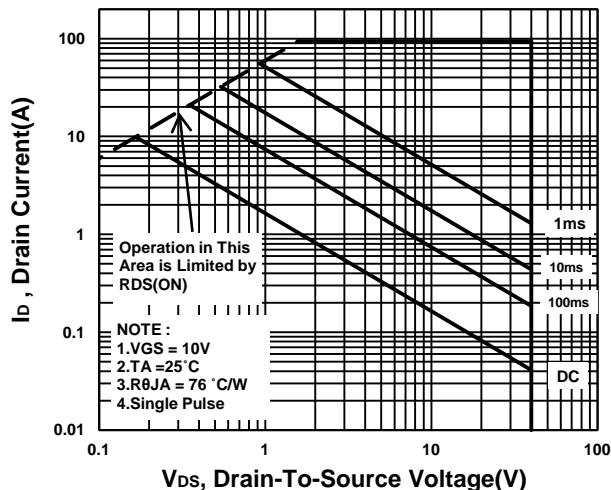
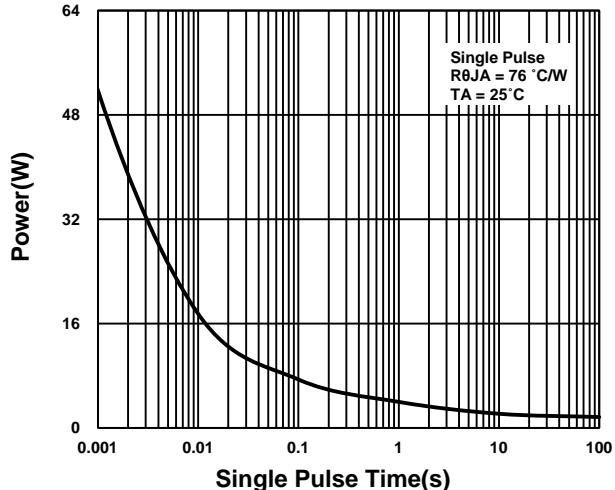
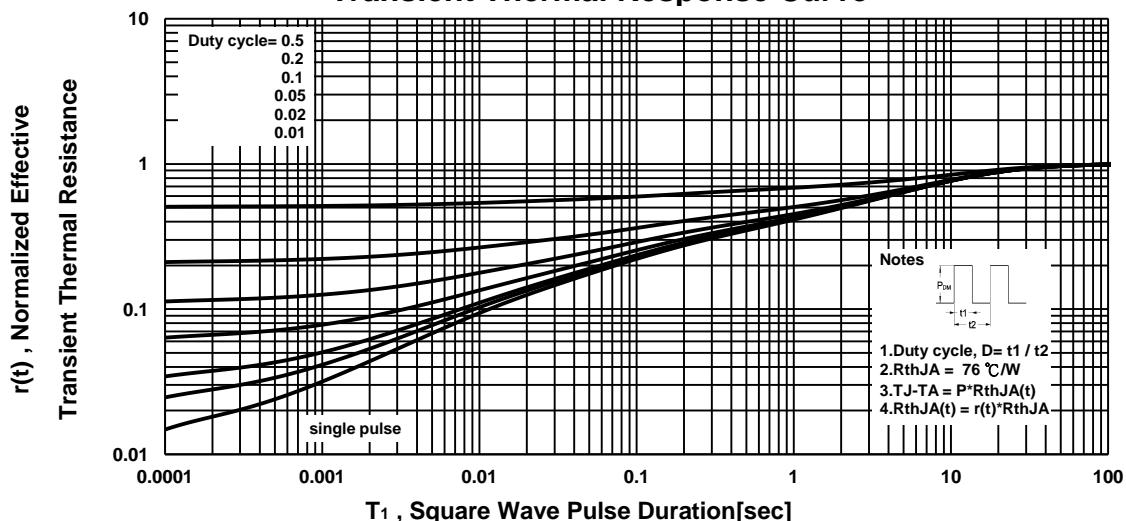


**On-Resistance VS Temperature**



**Capacitance Characteristic**



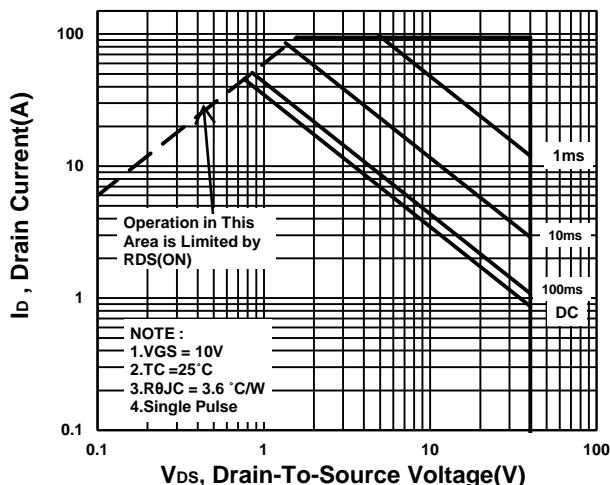
**NIKO-SEM****Dual N-Channel Enhancement Mode  
Field Effect Transistor****PK6M6DX  
PDFN 5x6P  
Halogen-Free & Lead-Free****Gate charge Characteristics****Source-Drain Diode Forward Voltage****Safe Operating Area****Single Pulse Maximum Power Dissipation****Transient Thermal Response Curve**

**NIKO-SEM**

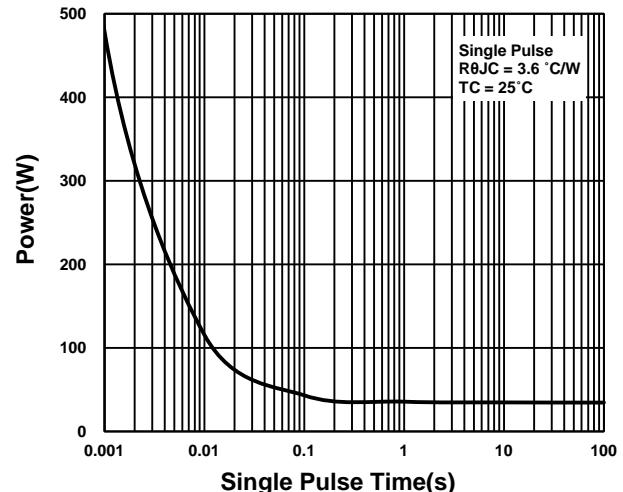
**Dual N-Channel Enhancement Mode  
Field Effect Transistor**

**PK6M6DX**  
**PDFN 5x6P**  
**Halogen-Free & Lead-Free**

**Safe Operating Area**



**Single Pulse Maximum Power Dissipation**



**Transient Thermal Response Curve**

