

PRODUCT SUMMARY

	$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
Q2	30V	3.2m Ω	25A
Q1	30V	3.3m Ω	25A

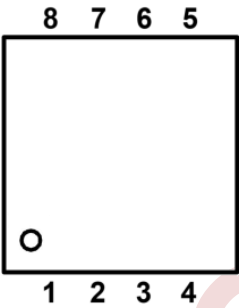


Features

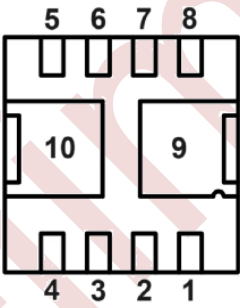
- Pb-Free, Halogen Free and RoHS compliant.
- Low $R_{DS(on)}$ to Minimize Conduction Losses.
- Ohmic Region Good $R_{DS(on)}$ Ratio.
- Optimized Gate Charge to Minimize Switching Losses.
- 100% UIS and Rg Tested.

Applications

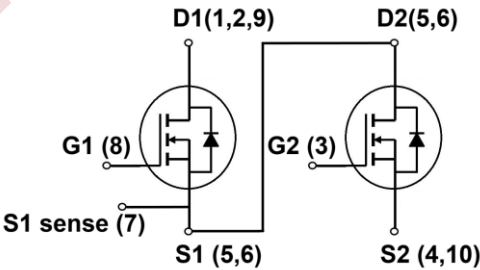
- Computing DC to DC converters.
- Communications DC to DC converters.
- General Purpose Point of load.



Top



Bottom



PINNING INFORMATION

PIN	SYMBOL	DESCRIPTION
1,2,9	D1	Drain 1
3	G2	Gate 2
4,10	S2	Source 2
5,6	D2 / S1	Drain 2 / Source 1
7	S1	Source sense 1
8	G1	Gate 1

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	Q2	Q1	UNITS
Drain-Source Voltage		V_{DS}	30	30	V
Gate-Source Voltage		V_{GS}	± 20	± 20	V
Continuous Drain Current	$T_C = 25\text{ }^{\circ}\text{C}$	I_D	83	82	A
	$T_C = 100\text{ }^{\circ}\text{C}$		52	52	
Pulsed Drain Current ¹		I_{DM}	142	142	
Continuous Drain Current ³	$T_A = 25\text{ }^{\circ}\text{C}$	I_D	25	25	
	$T_A = 70\text{ }^{\circ}\text{C}$		20	20	
Avalanche Current ⁴		I_{AS}	44	50	
Avalanche Energy ⁴		E_{AS}	29	38	mJ
Power Dissipation	$T_C = 25\text{ }^{\circ}\text{C}$	P_D	38	38	W
	$T_C = 100\text{ }^{\circ}\text{C}$		15	15	
Power Dissipation ³	$T_A = 25\text{ }^{\circ}\text{C}$	P_D	3.5	3.5	W
	$T_A = 70\text{ }^{\circ}\text{C}$		2.2	2.2	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150		$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE		SYMBOL		TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$t \leq 10\text{s}$	$R_{\theta JA}$	Q2		36	$^{\circ}\text{C} / \text{W}$
			Q1		36	
Junction-to-Ambient ²	Steady-State	$R_{\theta JA}$	Q2		60	
			Q1		60	
Junction-to-Case		$R_{\theta JC}$	Q2		3.3	
			Q1		3.3	

¹Pulse width limited by maximum junction temperature $T_{J(MAX)}=150^{\circ}\text{C}$.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^{\circ}\text{C}$. The value in any given application depends on the user's specific board design.

³The Power dissipation is based on $R_{\theta JA} t \leq 10\text{s}$ value.

⁴ $V_{DD}=50\text{V}$, $L=0.03\text{mH}$, $R_g=25\Omega$.

ELECTRICAL CHARACTERISTICS ($T_J = 25\text{ }^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT		
			MIN	TYP	MAX			
STATIC								
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	Q2	30		V		
		$V_{GS} = 0V, I_D = 250\mu A$	Q1	30				
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	Q2	1.3	1.65	2.2	V	
		$V_{DS} = V_{GS}, I_D = 250\mu A$	Q1	1.3	1.65	2.2		
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$	Q2			± 100	nA	
		$V_{DS} = 0V, V_{GS} = \pm 20V$	Q1			± 100		
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 30V, V_{GS} = 0V$	Q2			1	μA	
		$V_{DS} = 30V, V_{GS} = 0V$	Q1			1		
		$V_{DS} = 30V, V_{GS} = 0V, T_J = 55\text{ }^{\circ}C$	Q2			10		
		$V_{DS} = 30V, V_{GS} = 0V, T_J = 55\text{ }^{\circ}C$	Q1			10		
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 20A$	Q2		2.4	3.2	m Ω	
		$V_{GS} = 10V, I_D = 20A$	Q1		2.5	3.3		
		$V_{GS} = 4.5V, I_D = 16A$	Q2		3.5	4.7		
		$V_{GS} = 4.5V, I_D = 16A$	Q1		3.6	4.7		
Forward Transconductance	g_{fs}	$V_{DS} = 5V, I_D = 20A$	Q2		111		S	
		$V_{DS} = 5V, I_D = 20A$	Q1		81			
DYNAMIC								
Input Capacitance	C_{iss}	Q2 $V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$	Q2		1186		pF	
			Q1		1451			
Output Capacitance	C_{oss}	Q1 $V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$	Q2		747			
			Q1		408			
Reverse Transfer Capacitance	C_{rss}		Q2		13			
			Q1		23			
Gate Resistance	R_g	$f = 1MHz$	Q2		1.1		Ω	
			Q1		2.2			
Total Gate Charge ⁵	Q_g	Q2 $V_{DS} = 15V, V_{GS} = 10V,$ $I_D = 20A$	Q2		18		nC	
			Q1		22			
			Q2		7.9			
			Q1		9.7			
Gate-Source Charge ⁵	Q_{gs}		Q2		3.8			
			Q1		5.3			
Gate-Drain Charge ⁵	Q_{gd}		Q2		1.7			
			Q1		1.7			

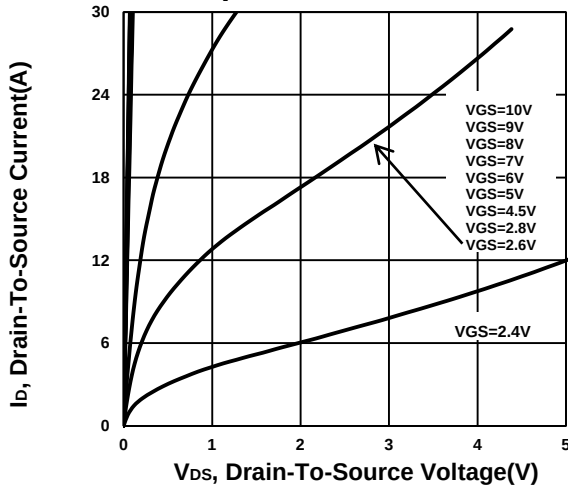
Turn-On Delay Time ⁵	t _{d(on)}	Q2 , V _{DS} = 15V I _D ≅ 20A V _{GS} = 10V, R _{GEN} =6Ω	Q2		9.4		nS
			Q1		11		
Rise Time ⁵	t _r		Q2		91		
			Q1		95		
Turn-Off Delay Time ⁵	t _{d(off)}	Q2		21			
		Q1		29			
Fall Time ⁵	t _f	Q2		110			
		Q1		110			
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T _J = 25 °C)							
Continuous Current	I _S		Q2			25	A
			Q1			25	
Forward Voltage	V _{SD}	I _F = 20A V _{GS} = 0V	Q2			1	V
		I _F = 20A, V _{GS} = 0V	Q1			1	
Reverse Recovery Time	t _{rr}	Q2	Q2			26	nS
		I _F = 20A dI _F /dt = 400A / μS	Q1			15	
Reverse Recovery Charge	Q _{rr}	Q1	Q2			39	nC
		I _F = 20A, dI _F /dt = 400A / μS	Q1			15	

⁵Independent of operating temperature.

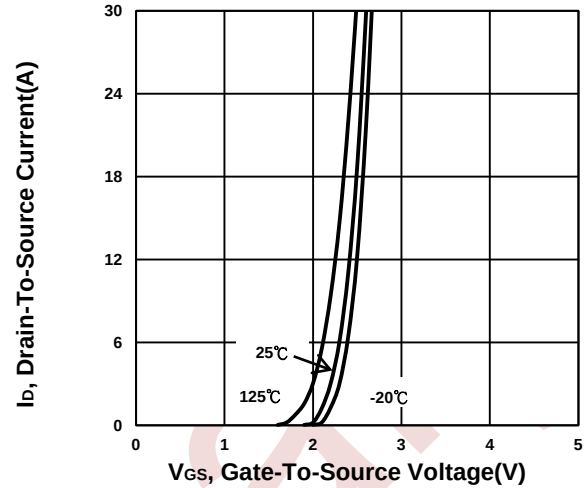
TYPICAL PERFORMANCE CHARACTERISTICS

Q2

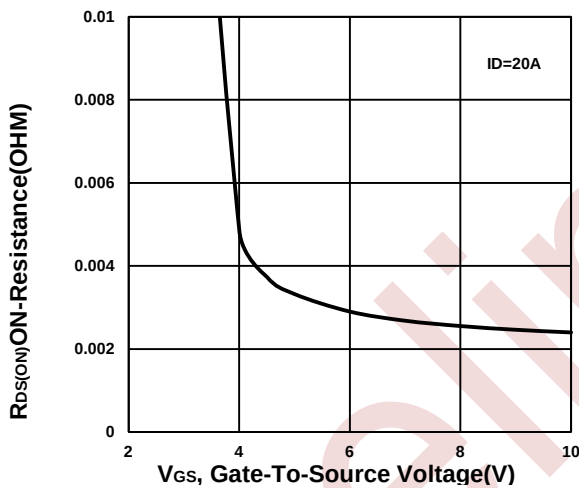
Output Characteristics



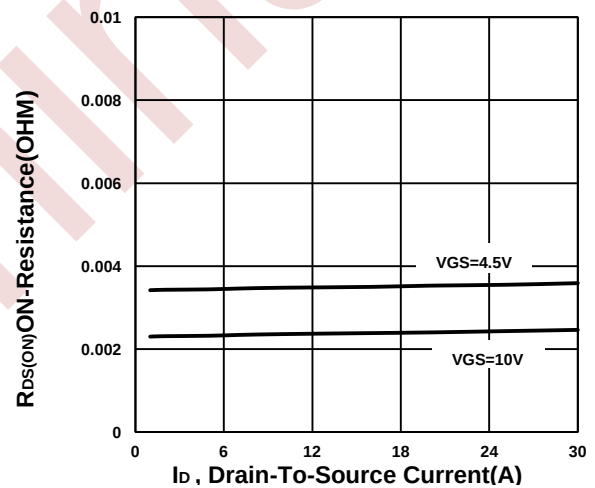
Transfer Characteristics



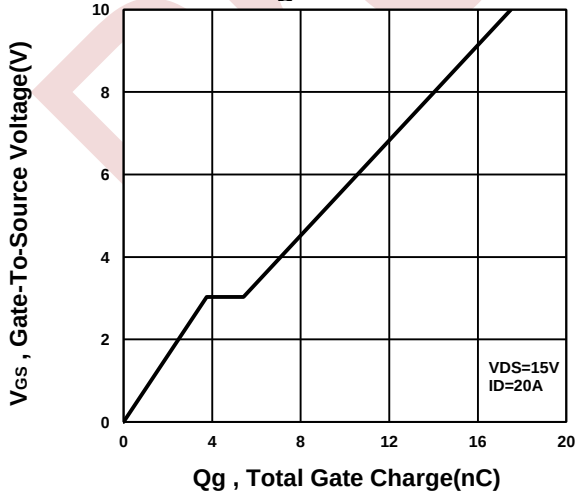
On-Resistance VS Gate-To-Source Voltage



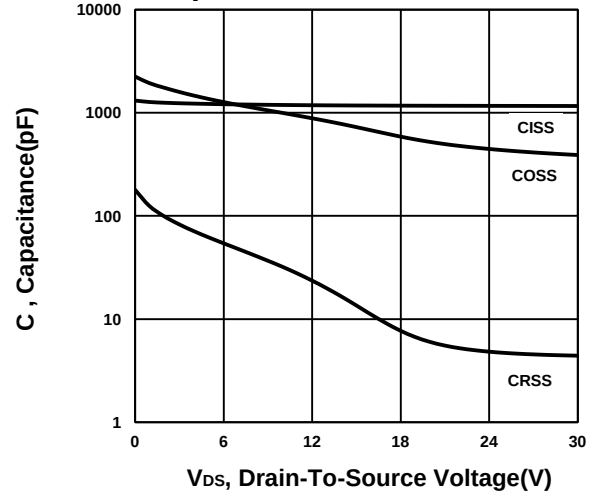
On-Resistance VS Drain Current



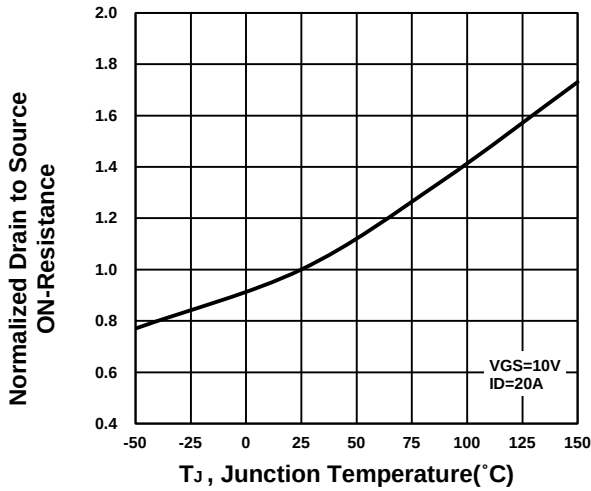
Gate charge Characteristics



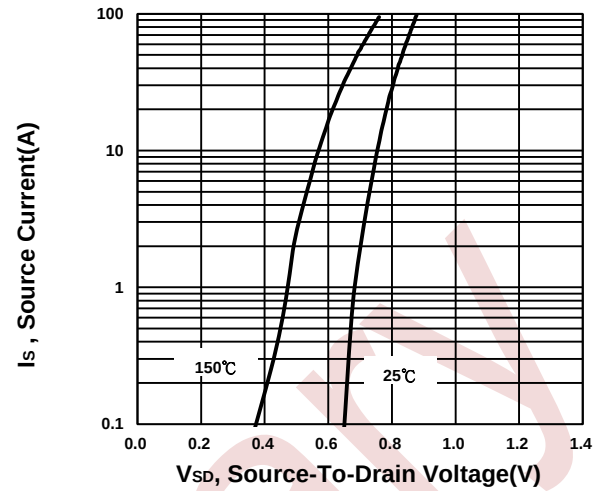
Capacitance Characteristic



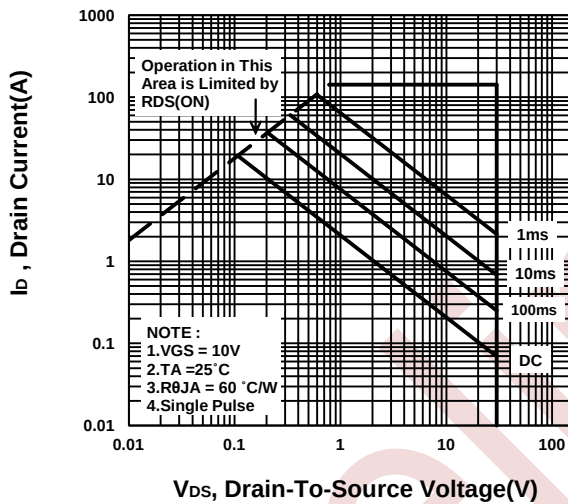
On-Resistance VS Temperature



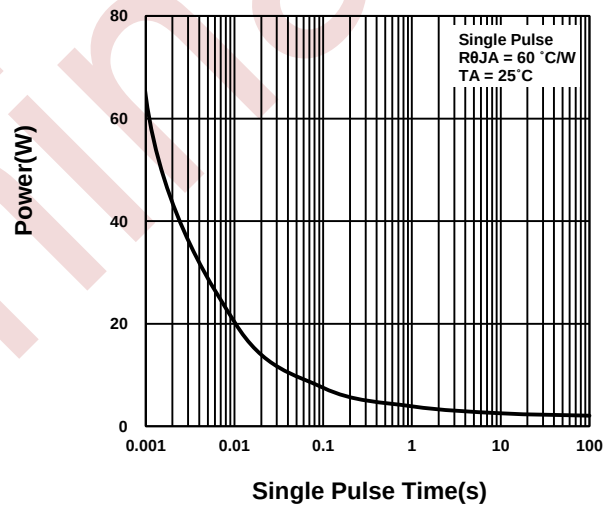
Source-Drain Diode Forward Voltage



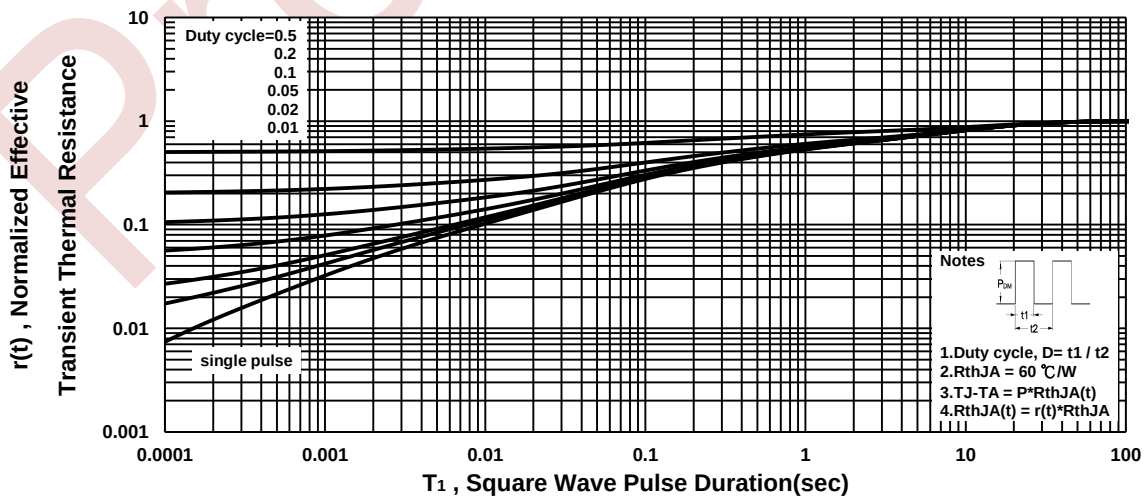
Safe Operating Area



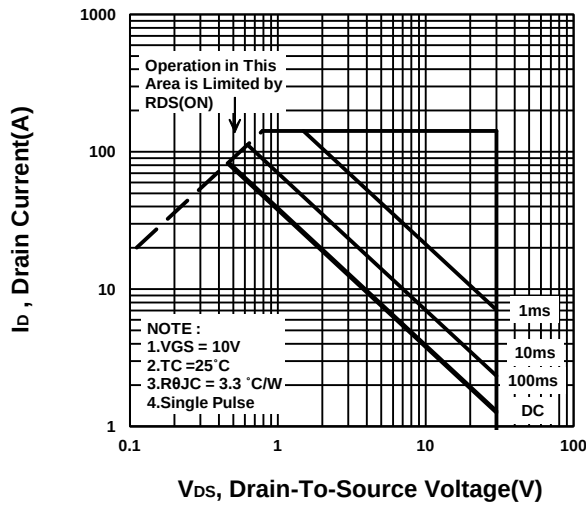
Single Pulse Maximum Power Dissipation



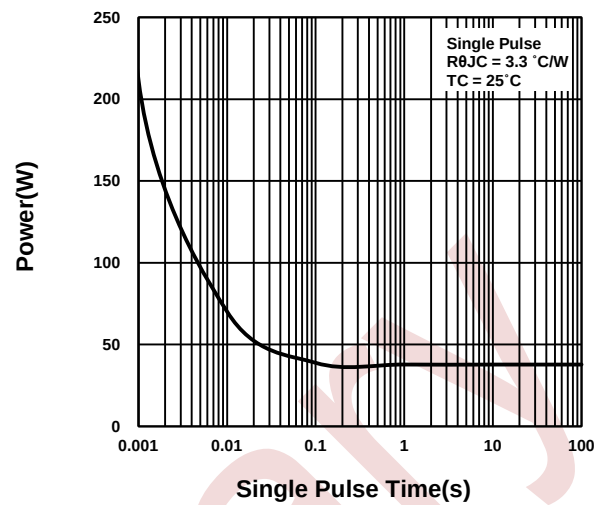
Transient Thermal Response Curve



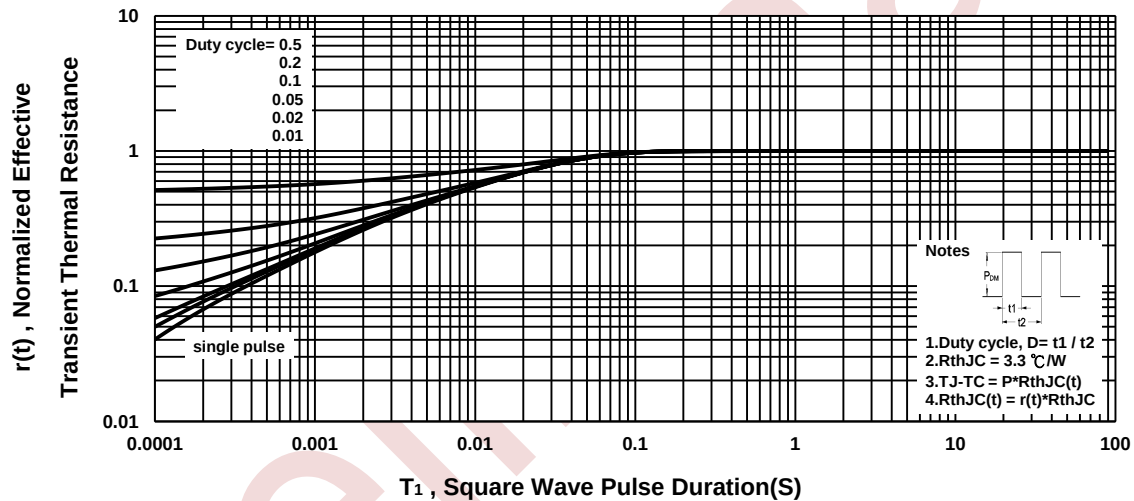
Safe Operating Area



Single Pulse Maximum Power Dissipation

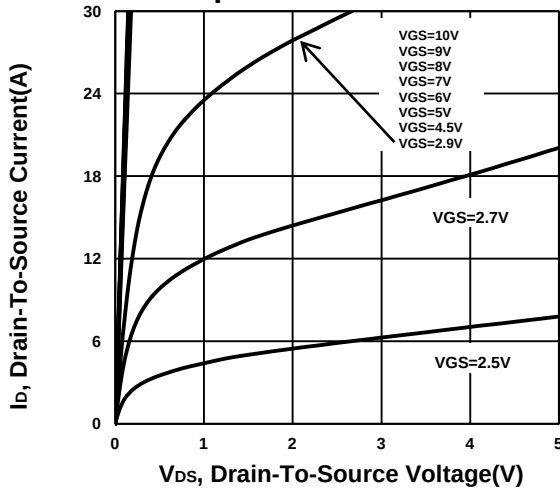


Transient Thermal Response Curve

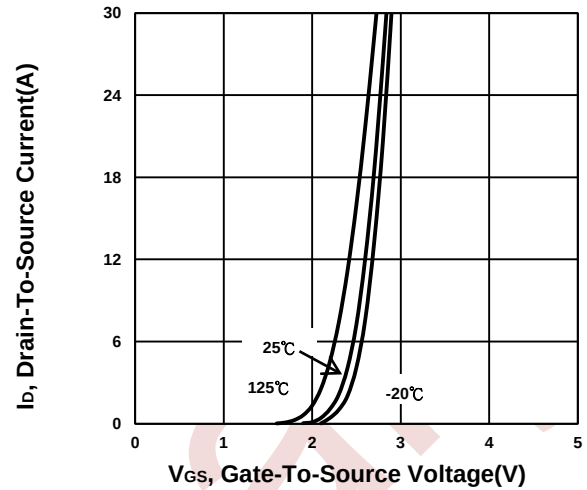


Q1

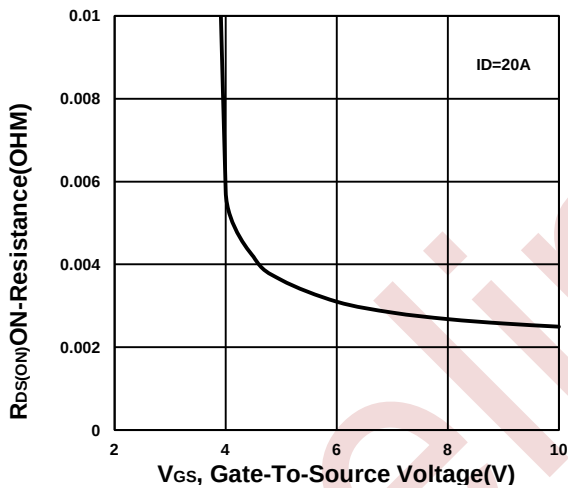
Output Characteristics



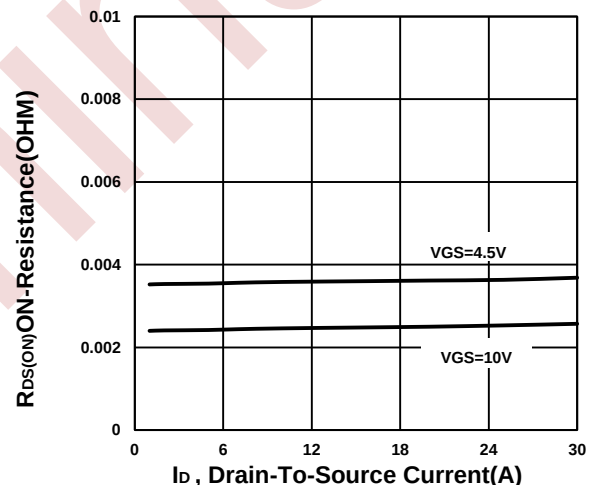
Transfer Characteristics



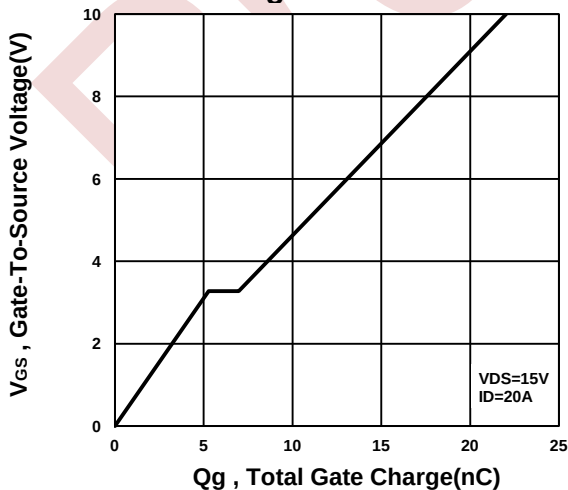
On-Resistance VS Gate-To-Source Voltage



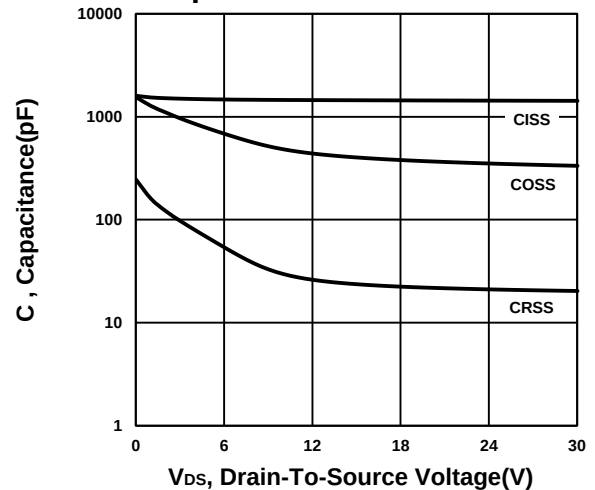
On-Resistance VS Drain Current



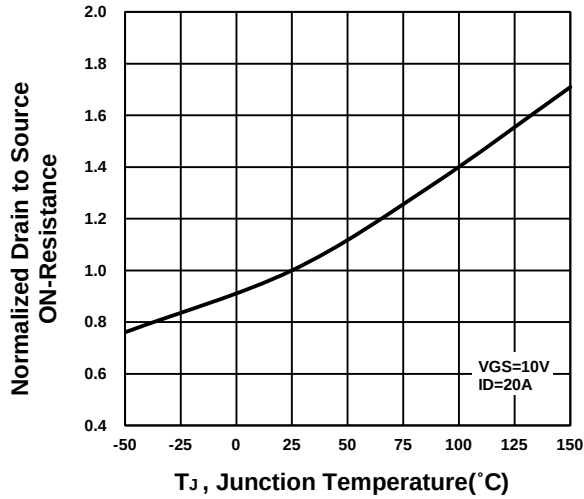
Gate charge Characteristics



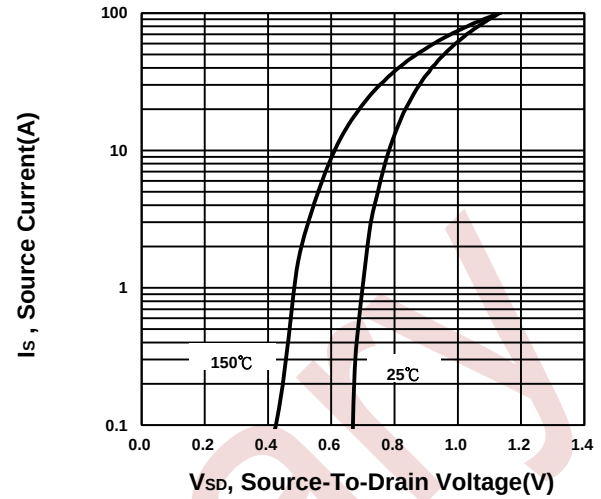
Capacitance Characteristic



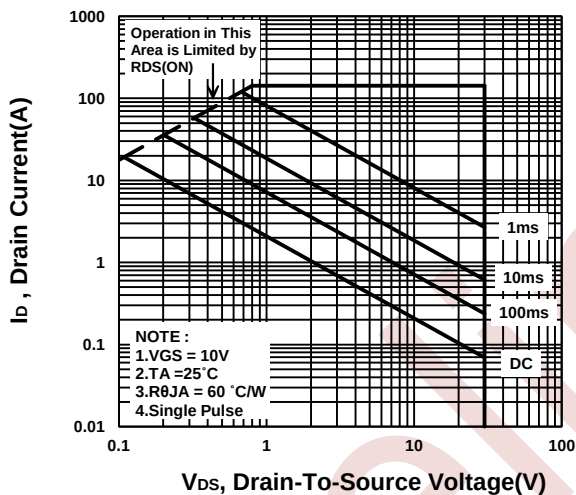
On-Resistance VS Temperature



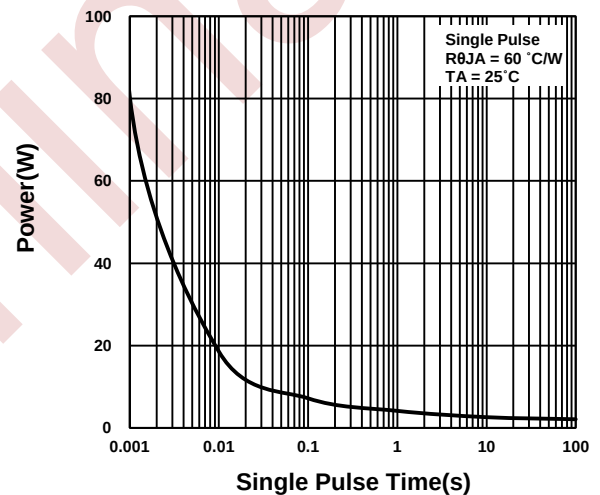
Source-Drain Diode Forward Voltage



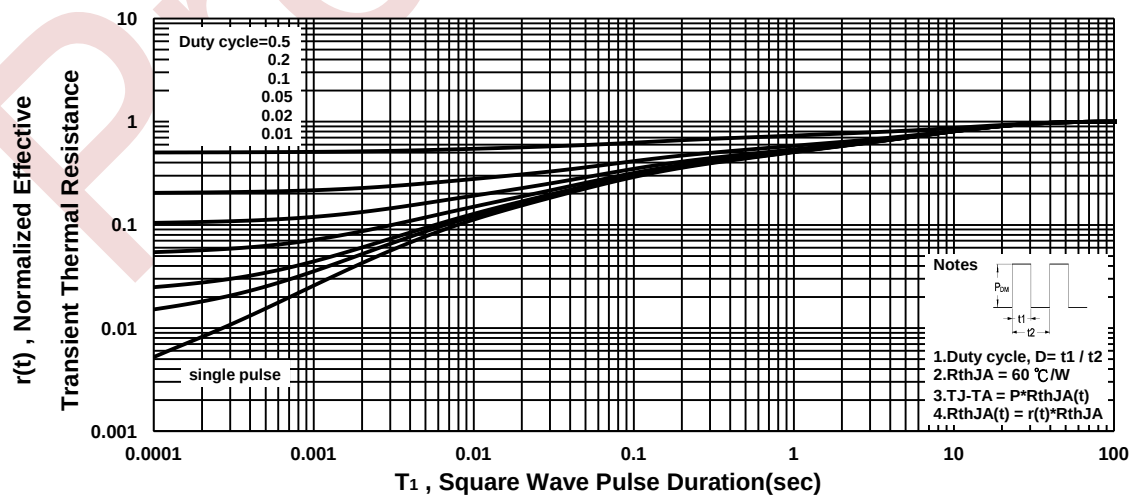
Safe Operating Area



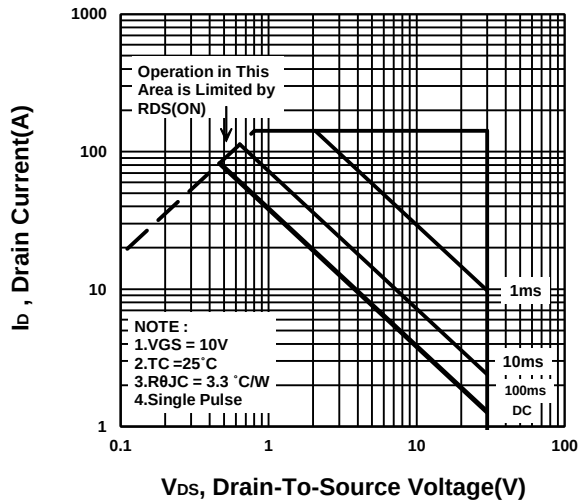
Single Pulse Maximum Power Dissipation



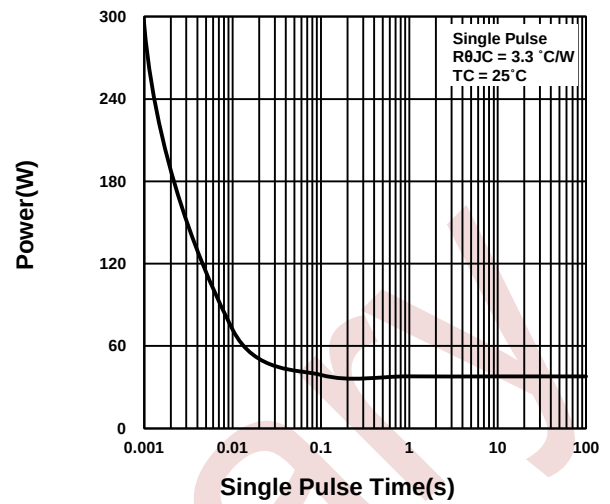
Transient Thermal Response Curve



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

